

# Coupled Inductor-Based Parallel Operation of a qZ-Source Full-Bridge DC-DC Converter

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## Abstract

This study presents a novel transformer isolated parallel connected quasi Z-source (qZ-source) full-bridge DC-DC converter that uses a coupled inductor in both the qZ-source network and output filter inductor. Unlike traditional voltage-fed or current-fed converters, the proposed converter can be open- and short-circuited without damaging switching devices. Therefore, the desired buck and boost functions can be achieved and converter reliability can be significantly improved. All the bulky inductors in the qZ-source network and output filter can also be minimized with the proposed inductor structures. A 4 kW prototype DC-DC converter is built and tested to verify the performance of the proposed converter.

**Key words:** Buck-boost converter, Coupled inductor, DC-DC converter, Full-bridge, Interleaved PWM, qZ-Source

## I. INTRODUCTION

Two types of full-bridge (FB) transformer-isolated DC-DC converters generally exist: a voltage-fed (V-fed) FB converter and a current-fed (C-fed) FB converter. Fig. 1 shows two traditional transformer-isolated DC-DC power conversion circuits. The V-fed FB converter [Fig. 1(a)] always has a buck (step-down) function, and the upper and lower switches that consist of a phase leg cannot be short-circuited [1]. Similarly, the C-fed FB converter [Fig. 1(b)] always has a boost (step-up) function, and the upper and lower switches that consist of a phase leg cannot be open-circuited.

Thus, the two traditional DC-DC converters shown in Fig. 1 can only produce an output voltage that is smaller than or larger than the input voltage. Furthermore, they cannot be either short- or open-circuited. Consequently, traditional V-fed and C-fed converters are susceptible to electromagnetic interference (EMI) noise, which considerably impairs their system reliability. Quasi Z-source (qZ-source) or Z-source

converters or inverters are introduced to overcome the aforementioned problems [1]-[3]. Fig. 2 shows an example of a transformer-isolated qZ-source FB DC-DC converter.

By using a qZ-source network that consists of a diode, two inductors, and two capacitors, the main switches ( $S_1$  to  $S_4$ ) can be short- and open-circuited without damaging the switching devices. Therefore, the desired buck and boost functions can be achieved, which is appropriate for converters with a having wide input voltage range.

Parallel-connected power converters operate via a general method that easily increases power level in the power electronics area. Through parallel operation, low current rating semiconductor devices can be used without over-sizing a converter module [4]-[6]. When multiple converters are connected in parallel, an interleaved pulse-width modulation (PWM) method is widely used to decrease input and output current ripples. Consequently, this method can reduce input and output capacitor volumes and increase converter efficiency.

Although a qZ-source converter is resistant to EMI noise because of the presence of a qZ-source network, the two inductors and two capacitors in the qZ-source network are heavy and bulky even if the two inductors  $L_1$  and  $L_2$  are coupled. The switching frequency of the converter should be increased to decrease the size of passive components. Increasing switching frequency, however, reduces converter

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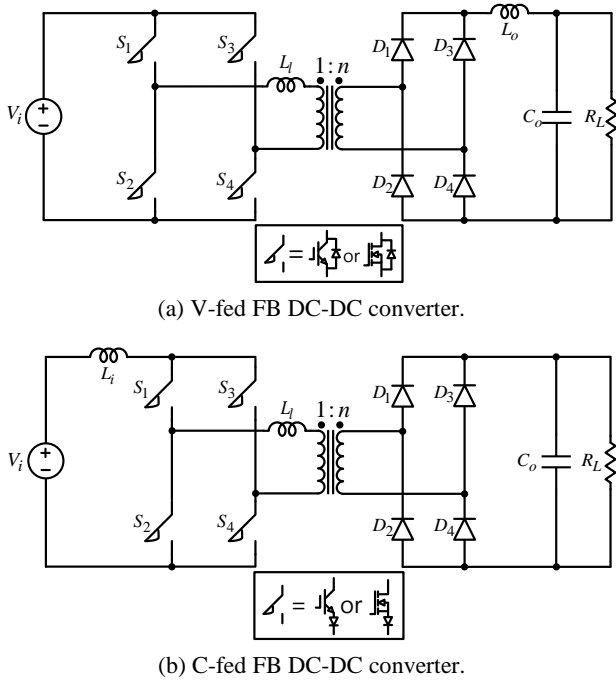


Fig.1. Traditional FB DC-DC converters.

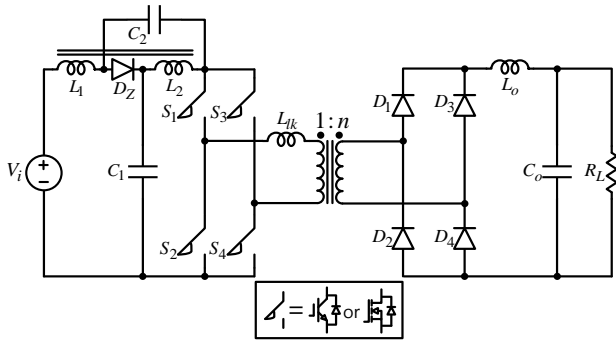


Fig. 2. qZ-source FB DC-DC converter.

efficiency.

This study presents a novel transformer-isolated and parallel-connected FB DC-DC converter a qZ-source network is used to achieve the desired buck and boost functions and increase converter reliability. A specially designed coupled inductor is also used in both the qZ-source network and output filter inductor to minimize inductor current ripple and volume significantly.

With this structure, the proposed DC-DC converter exhibits the following advantages over conventional converters.

- The proposed converter can be short- and open-circuited without damaging switching devices.
- The converter can operate in a wide input voltage range because it has buck and boost functions.
- The converter can be easily applied to a high power converter system through parallel operation.

This paper is organized as follows. The principal operation

of the proposed converter is reviewed in Section II. A study on the comparative analysis of current ripples on the non-coupled inductor and coupled inductor is presented in Section III. Experiment results to verify are illustrated in Section IV. Lastly some conclusions and application of the proposed converter are given in Section V.

## II. PRINCIPAL OPERATION OF THE PROPOSED PARALLEL CONNECTED qZ-SOURCE FB DC-DC CONVERTER

Fig. 3 shows the overall circuit configuration of the proposed DC-DC converter. Two identical transformer-isolated qZ-source FB DC-DC converters are connected in parallel to increase the power rating of the converter. Conventional two-phase interleaved PWM switching is applied. Therefore, the top and bottom converters are switched at  $90^\circ$  apart, which results in a  $180^\circ$  phase shift at the top and bottom qZ-source networks and output filter inductors because of the FB structure.

As mentioned in Section I, the inductors in the qZ-source network are heavy and take a significant portion of the entire converter. All the inductors ( $L_1$  to  $L_4$ ) at the top and bottom qZ-source networks are coupled to decrease inductor size. The two output filter inductors ( $L_{o1}$  and  $L_{o2}$ ) are also coupled by following a similar method.

### A. Buck Mode Operation ( $V_i > V_o$ )

When  $V_i > V_o$ , the proposed converter operates in buck mode. No shoot-through interval occurs in buck mode, and thus, diodes  $D_{z1}$  and  $D_{z2}$  in the qZ-source networks are always conducting. No energy is stored in the qZ-source inductors; hence,

$$V_{C_1} = V_{C_3} = v_{pn1} = v_{pn2} = V_i \quad (1)$$

Consequently, the proposed converter operates as the conventional parallel-connected V-fed FB DC-DC converter that uses PWM control. Defining  $D$  as the effective duty ratio in the output filter side, the voltage gain of the proposed converter in this mode is expressed as follows:

$$\frac{V_o}{V_i} = nD \quad (2)$$

where  $n$  is the transformer turns ratio.

### B. Boost Mode Operation ( $V_i < V_o$ )

In the boost mode, a shoot-through interval occurs at the gate signal, and energy is stored in the qZ-source inductors. The well-known voltage equations in the shoot-through (boost) mode are as follows:

$$V_{C_1} = V_{C_3} = \left( \frac{1 - D_{sh}}{1 - 2D_{sh}} \right) V_i, \quad (3)$$

$$V_{pn1} \cdot V_{pn2} = \left( \frac{1}{1 - D_{sh}} \right) V_i, \quad (4)$$

where  $D_{sh}$  is the switch shoot-through duty ratio of the

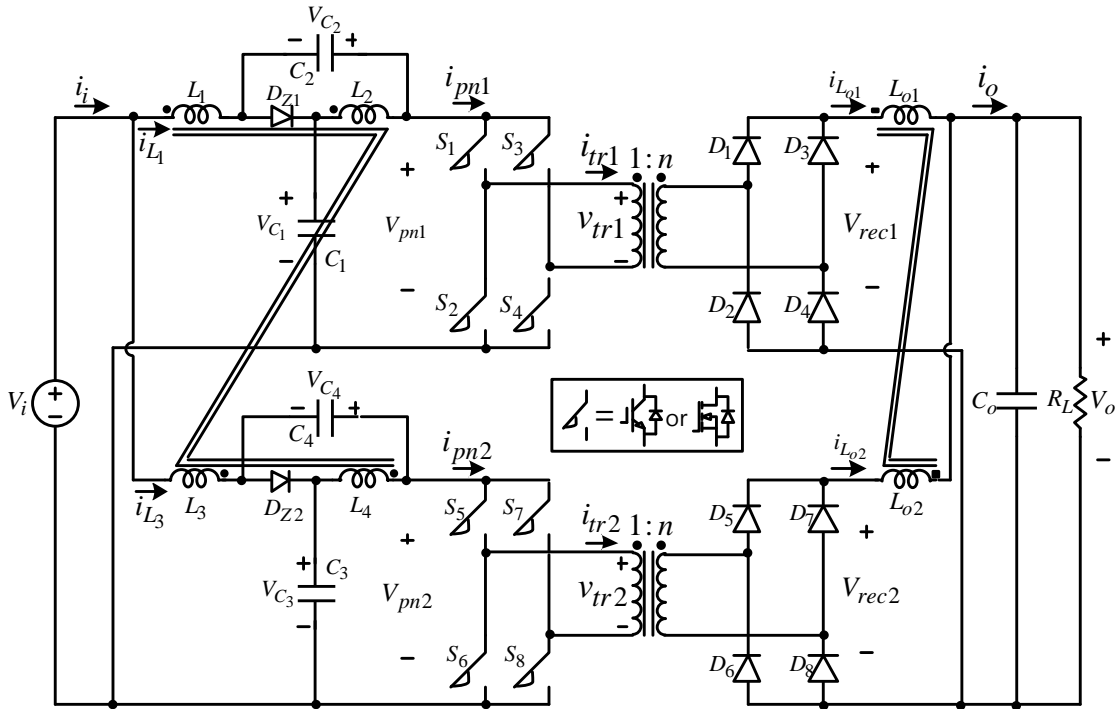


Fig. 3. Proposed parallel-connected qZ-source FB DC-DC converter that uses a coupled inductor.

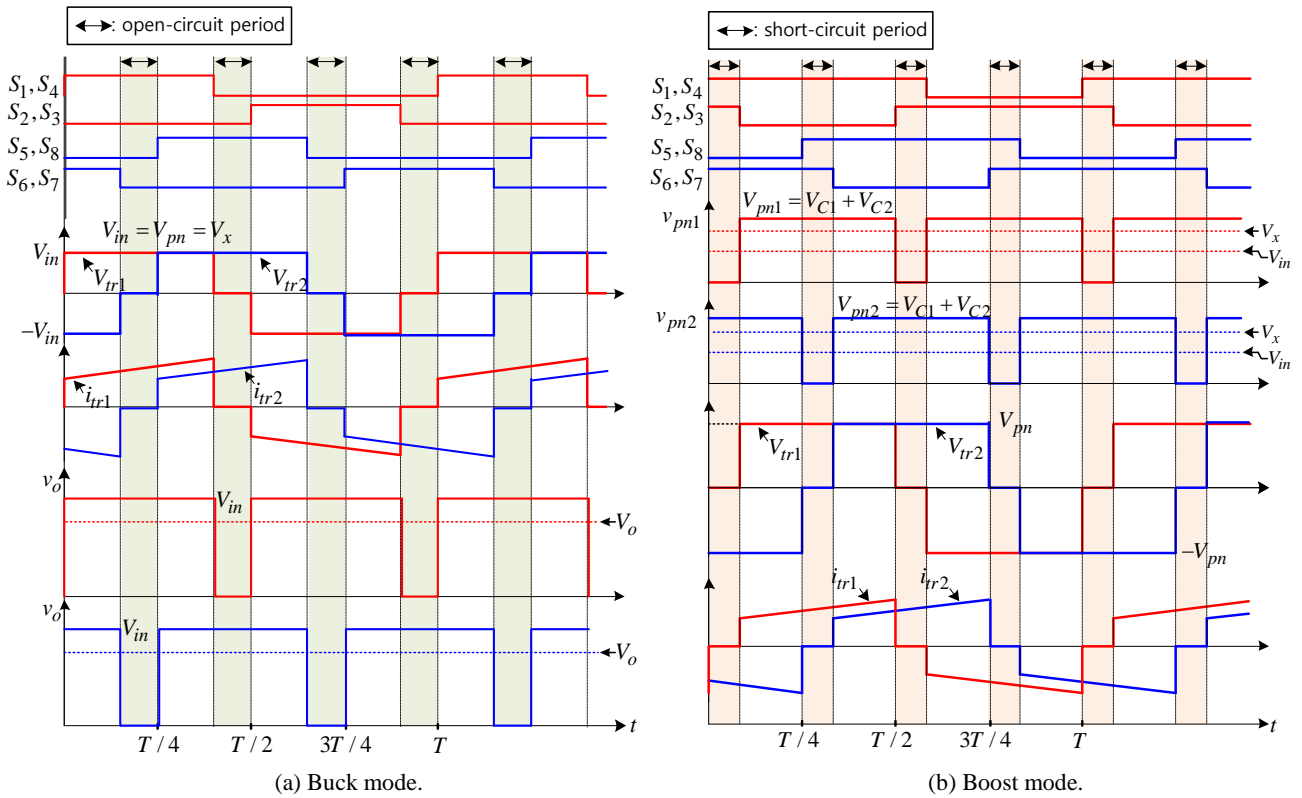


Fig. 4. Key waveforms of the proposed parallel-connected qZ-source FB DC-DC converter.

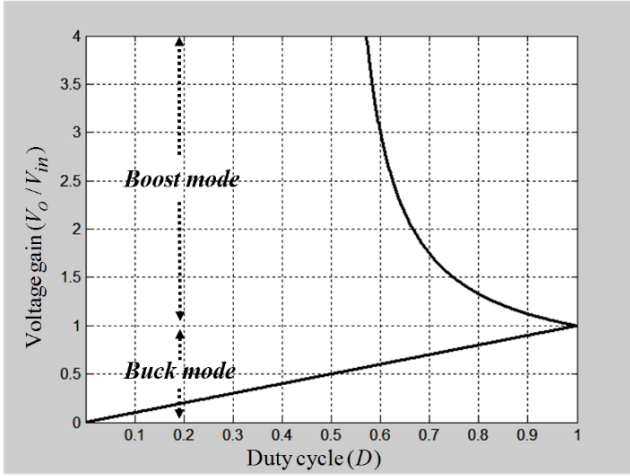


Fig. 5. Voltage gain of the proposed converter ( $n=1$ ).

converter that is defined as  $D_{sh} = 1 - D$ .

Therefore, the voltage gain of the proposed converter in the boost mode is expressed as follows:

$$V_o = n \left( \frac{1 - D_{sh}}{1 - 2D_{sh}} \right) V_i . \quad (5)$$

Given that  $D_{sh} = 1 - D$ , Equation (5) can be rewritten as follow:

$$V_o = n \left( \frac{D}{2D - 1} \right) V_i . \quad (6)$$

Fig. 5 shows the voltage gain plot of the proposed converter as  $D$  varies when  $n = 1$ . As expected, the desired buck- and boost functions can be achieved by modulating  $D$ . Fig. 4 shows the key waveforms of the proposed converter in both buck and boost modes. Given the interleaving effect, the output (or input) current ripple is reduced. However, all inductor current ripples are not reduced even with the interleaving action. Thus, they remain bulky and heavy. A coupled inductor is used in this study to decrease inductor current ripples.

### III. THE COUPLED INDUCTOR AND ITS EFFECTS

This section discusses the effect of the coupled inductor on decreasing ripple currents. Fig. 3 shows that, the four inductors in the qZ-source networks and the two inductors in the output filter are coupled. The two output filter inductors ( $L_{o1}$ ,  $L_{o2}$ ) are first selected for the analysis because their operation is similar to that of the conventional two-phase interleaved buck converter [7]-[13].

The two main issues of interest in the interleaved buck converter is the inductor (phase) current ripple and output current ripple, which is the sum of the two inductor currents. The current ripples in a non-coupled inductor are first analyzed to compare the ripple reduction effect.

#### A. Non-Coupled Inductor

Fig. 6 shows the voltage waveforms across  $L_{o1}$  and  $L_{o2}$ . Four operating states are observed. The detailed voltage and

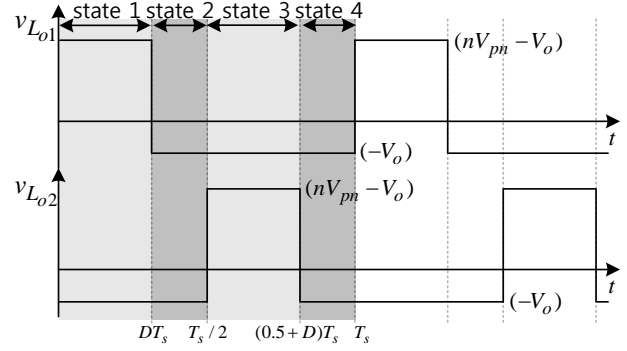


Fig. 6. Voltage waveform of the output filter inductor.

TABLE I

OUTPUT INDUCTOR CURRENT VARIATION OF A NON-COUPLED INDUCTOR

	State 1	State 2	State 3	State 4
$i_{L_{o1}}$	increasing	decreasing	decreasing	decreasing
$i_{L_{o2}}$	decreasing	decreasing	increasing	decreasing
$i_o$	increasing	decreasing	increasing	decreasing

TABLE II

CURRENT RIPPLE COMPARISON

	Single-phase		Two-phase	
	$D < 0.5$	$D > 0.5$	$D < 0.5$	$D > 0.5$
Inductor current ripple	$\frac{V_o}{L}(1-D)T_s$	$\frac{V_o}{L}DT_s$	$\frac{V_o}{L}(1-D)T_s$	$\frac{V_o}{L}DT_s$
Output current ripple	$\frac{V_o}{L}(1-D)T_s$	$\frac{V_o}{L}DT_s$	$\frac{V_o}{L}(1-2D)T_s$	$\frac{V_o}{L}(2D-1)T_s$

current direction in each state is summarized in Table I by assuming that  $v_{rec1} = v_{rec2} = nV_{pn}$ .

Given that the two inductors are non-coupled, no interaction occurs between them and the inductor current ripple is calculated as follows:

$$\Delta i_{L_{o1}} = \frac{V_o}{L_{o1}}(1-D)T_s, \quad (7)$$

$$\Delta i_{L_{o2}} = \frac{V_o}{L_{o2}}(1-D)T_s . \quad (8)$$

If  $L_{o1} = L_{o2} = L$ , then the inductor current ripple in a non-coupled case is expressed as follows:

$$\Delta i_{L_o} = \frac{V_o}{L}(1-D)T_s . \quad (9)$$

Considering that the output current is the sum of  $i_{L_1}$  and  $i_{L_2}$ , then it increases in State 1 and decreases in State 2. This cycle is repeated in States 3 and 4. Consequently, the output current ripple occurs twice at a given period  $T_s$ . The equation for the output current ripple is as follows:

$$\Delta i_o = \frac{V_o}{L}(1-2D)T_s . \quad (10)$$

Equations (7) to (10) are valid when  $D < 0.5$ . When

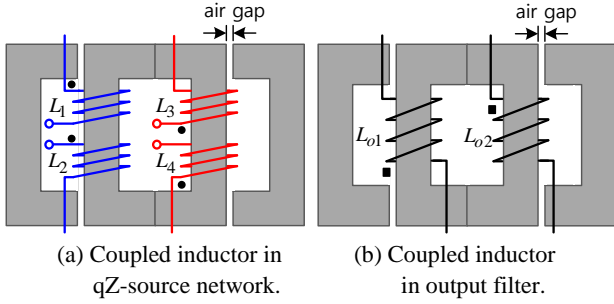


Fig. 7. Coupled inductor design for the proposed converter

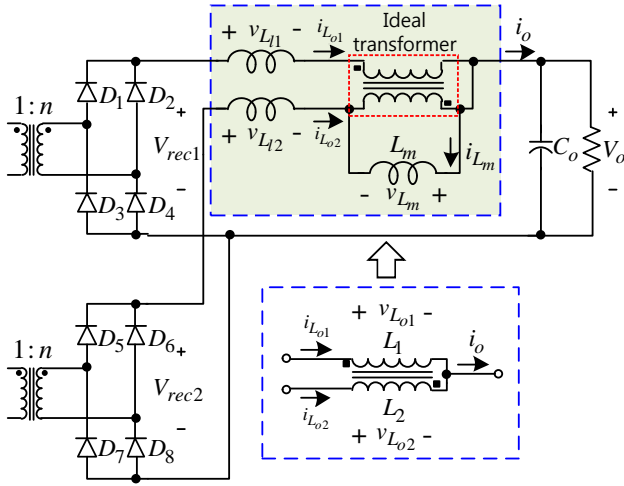


Fig. 8. Secondary side of the proposed converter with a coupled inductor.

$D > 0.5$ , the equations can be derived similarly as summarized in Table II [7]. The inductor and output current ripples of the two-phase interleaved converter are compared with those of its single-phase counterpart to determine the ripple current reduction effect of the former. The results are summarized in Table II. Unlike in the single-phase converter, the output current ripple in the two-phase case is reduced significantly and its maximum effect is reached when  $D$  approaches 0.5. The inductor current ripples, however, remain unchanged [8], [9].

### B. Coupled Inductor

Fig. 7 depicts the proposed coupled inductor structures for both the qZ-source networks and output filter inductors. The secondary side of the transformer of the proposed converter is redrawn (Fig. 8.) to analyze the coupled inductor. The coupled inductor is represented as an ideal 1:1 transformer, two leakage inductances ( $L_{l1}, L_{l2}$ ), and a magnetizing inductance ( $L_m$ ). Notably, the ideal transformer is connected out of phase with the polarity dots on the opposite ends [8]. Therefore,

$$i_{L_m} = i_{L_{o1}} - i_{L_{o2}} \quad (11)$$

Similar to in the non-coupled case, four operation states exist in the coupled case. Regardless of the operation states, the following relationships are always applied:

$$v_{L_{o1}} = v_{L_{l1}} + v_{L_m}, \quad v_{L_{o2}} = v_{L_{l2}} - v_{L_m}, \quad (12)$$

$$v_{L_m} = L_m \frac{d}{dt}(i_{L_{o1}} - i_{L_{o2}}). \quad (13)$$

1) *State 1* [ $0 \sim DT_s$ ]: Based on Figs. 6 and 8, the following equation is applied in State 1:

$$v_{L_{o1}} = nV_{pn} - V_o, \quad v_{L_{o2}} = -V_o. \quad (14)$$

From Equations (12) to (14),  $v_{L_{l1}}$  and  $v_{L_{l2}}$  are expressed as follows:

$$v_{L_{l1}} = v_{L_{o1}} - v_{L_m} = (nV_{pn} - V_o) - v_{L_m} = L_{l1} \frac{d}{dt} i_{L_{o1}}, \quad (15)$$

$$v_{L_{l2}} = v_{L_{o2}} + v_{L_m} = (-V_o) + v_{L_m} = L_{l2} \frac{d}{dt} i_{L_{o2}}. \quad (16)$$

By assuming that  $L_{l1} = L_{l2} = L_l$  and substituting Equations (15) to (16) into Equation (13),  $v_{L_m}$  is expressed as follows:

$$v_{L_m} = \left( \frac{L_m}{2L_m + L_l} \right) nV_{pn}. \quad (17)$$

Base on Equations (15) to (17), defining  $p = L_m/L_l$  yields the following:

$$\Delta i_{L_{o1}} = \frac{V_o}{L_l} \left( \frac{1+p}{1+2p} - D \right) T_s, \quad (18)$$

$$\Delta i_{L_{o2}} = \frac{V_o}{L_l} \left( \frac{p}{1+2p} - D \right) T_s. \quad (19)$$

The output current ripple is expressed as follows:

$$\Delta i_o = \frac{V_o}{L_l} (1 - 2D) T_s. \quad (20)$$

2) *State 2* [ $DT_s \sim T_s/2$ ]: In State 2,  $v_{L_{o1}} = v_{L_{o2}} = -V_o$ . Thus,

$$v_{L_{l1}} = v_{L_{o1}} - v_{L_m} = -V_o - v_{L_m} = L_{l1} \frac{d}{dt} i_{L_{o1}}, \quad (21)$$

$$v_{L_{l2}} = v_{L_{o2}} + v_{L_m} = -V_o + v_{L_m} = L_{l2} \frac{d}{dt} i_{L_{o2}}. \quad (22)$$

When Equations (21) to (22) are substituted into Equation (13), then  $v_{L_m} = 0$ . Hence,

$$\Delta i_{L_{o1}} = \Delta i_{L_{o2}} = \frac{V_o}{L_l} \left( \frac{1}{2} - D \right) T_s, \quad (23)$$

$$\Delta i_o = \frac{V_o}{L_l} (1 - 2D) T_s. \quad (24)$$

3) *State 3* [ $T_s/2 \sim (T_s/2 + DT_s)$ ]

State 3 is similar to State 1. The only difference is that  $v_{L_{o1}} = -V_o$  and  $v_{L_{o2}} = nV_{pn} - V_o$ . Using methods similar to those in State 1, the inductor and output current ripple in State 3 are derived as follows:

$$\Delta i_{L_{o1}} = \frac{V_o}{L_l} \left( \frac{p}{1+2p} - D \right) T_s, \quad (25)$$

TABLE III  
CURRENT RIPPLE OF THE FILTER INDUCTOR AND OUTPUT

	State 1	State 2	State 3	State 4
$\Delta i_{L_{o1}}$	$\frac{V_o}{L_l} \left( \frac{1-(k+1)D}{1+k} \right) T_s$	$\frac{V_o}{L_l} \left( \frac{1}{2} - D \right) T_s$	$\frac{V_o}{L_l} \left( \frac{k-(k+1)D}{1+k} \right) T_s$	$\frac{V_o}{L_l} \left( \frac{1}{2} - D \right) T_s$
$\Delta i_{L_{o2}}$	$\frac{V_o}{L_l} \left( \frac{k-(k+1)D}{1+k} \right) T_s$		$\frac{V_o}{L_l} \left( \frac{1-(k+1)D}{1+k} \right) T_s$	
$\Delta i_o$	$\frac{V_o}{L_l} (1-2D) T_s$			

$$\Delta i_{L_{o2}} = \frac{V_o}{L_l} \left( \frac{1+p}{1+2p} - D \right) T_s, \quad (26)$$

$$\Delta i_o = \frac{V_o}{L_l} (1-2D) T_s. \quad (27)$$

4) State 4  $[(T_s/2 + DT_s) \sim T_s]$ : State 4 is exactly the same as State 2.

According to the state analysis of the coupled inductor, the output current ripple in the coupled case also occurs twice at a given period  $T_s$ . The coupling ratio  $p$  does not affect the output current ripple [7]. Equations (10) and (20) show that if  $L_l$  is equal to the value of the individual inductor  $L$  in the non-coupled case, then the output current ripples in both the non-coupled and coupled inductors are the same.

Unlike those in the non-coupled case, the inductor current ripples in the coupled inductor change direction twice per cycle as the output current ripple. The two inductor currents become equal and in-phase as  $p$  increases to infinity (perfect coupling). Thus, the coupling ratio  $p$  has a significant effect on inductor current ripples, although it does not affect output current ripples [7].

Representing  $L_l$  and  $L_m$  in the coupled inductor (or transformer) in terms of the coupling coefficient  $k$  rather than the coupling ratio  $p$  is more intuitive because we are more familiar with  $k$ . Based on the general coupled inductor theory,  $L_l$  and  $L_m$  are represented as follows [14]:

$$L_l = (1-k)L_s, \quad L_m = kL_s, \quad (28)$$

where  $L_s$  is the self-inductance of the coupled inductor.

Therefore, the relationship between  $p$  and  $k$  is as follows:

$$p = \frac{k}{1-k}. \quad (29)$$

Using Equation (29), Equations (18) and (19) can be rewritten as shown in Table III. The inductor current ripple depends on the  $D$  and  $k$  values. Therefore, the ratio of the inductor current ripple of a coupled inductor ( $\Delta i_{L,cp}$ ) normalized with respect to that of a non-coupled inductor ( $\Delta i_{L,nc}$ ) is expressed as follows:

$$\frac{\Delta i_{L,cp}}{\Delta i_{L,nc}} = \frac{1 - \left( \frac{D}{1-D} \right) k}{1+k}. \quad (30)$$

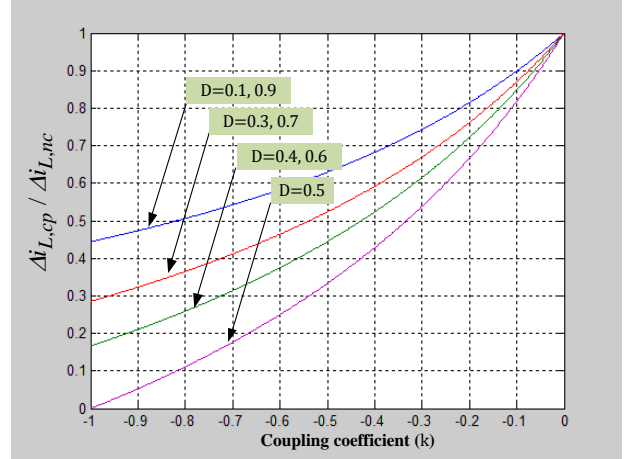


Fig. 9. Inductor current ripple of a coupled inductor normalized with respect to a non-coupled inductor.

The relationship in Equation (30) is plotted in Fig. 9. As shown in the figure, a small current ripple can be achieved with a strong coupling coefficient. The same analysis can be readily applied to the coupled inductor in the qZ-source network. The only difference is the voltage across the qZ-source network inductors.

Fig. 7(a) shows, that the four inductors ( $L_1$  to  $L_4$ ) in the qZ-source network are coupled.  $L_1$  and  $L_2$  are wound on the same leg. Similarly,  $L_3$  and  $L_4$  are wound on the other leg. Given that the operations of top and bottom qZ-source networks are the same as that of the output coupled inductor, the same ripple reduction effect occurs in the qZ-source inductor currents and in the input current ripple.

#### IV. EXPERIMENT RESULTS

A 4 kW prototype converter is built and tested to verify the performance of the proposed converter. Table IV illustrates the electrical specifications of the proposed converter, including the detailed parameters of the coupled inductors. The transformer turns ratio is set to 1:1.

Fig. 10 shows the 4 kW proposed converter and the photo of the coupled inductor built for this study. similar core structures are used for both the qZ-source inductors and output filter inductors. Figs. 11 and 12 show the transformer

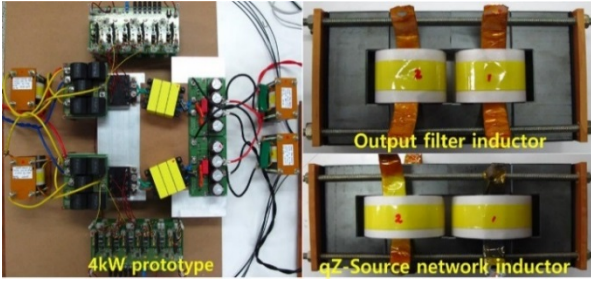
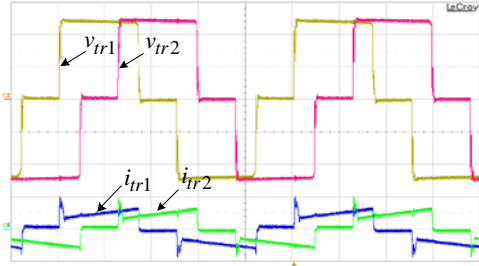
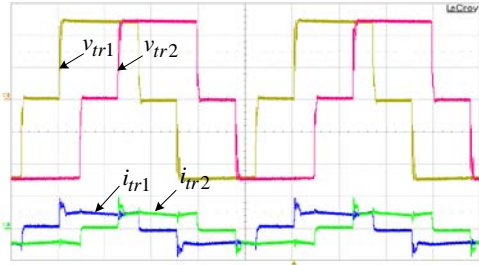


Fig. 10. Photos of the 4 kW prototype converter and the coupled inductors.

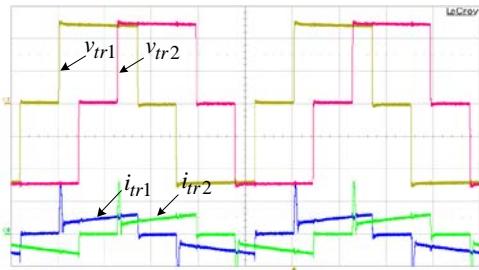


(a) Non-coupled inductor.

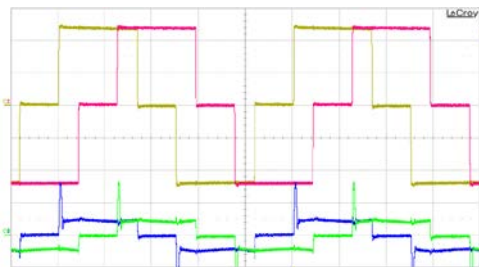


(b) Coupled inductor.

Fig. 11. Waveforms of transformer voltages and currents at buck mode. ( $v_{tr1,2}$  [250V/div.],  $i_{tr1,2}$  [10A/div.], [10 $\mu$ s/div.],  $V_i = 600V$ ,  $V_o = 400V$  and  $P_o = 4kW$ )

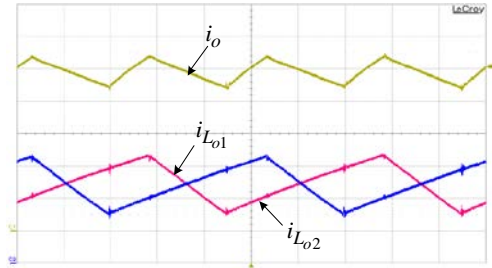


(a) Non-coupled inductor.

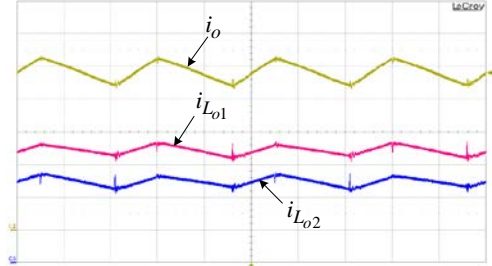


(b) Coupled inductor.

Fig. 12. Waveforms of transformer voltages and currents at boost mode. ( $v_{tr1,2}$  [250V/div.],  $i_{tr1,2}$  [10A/div.], [10 $\mu$ s/div.],  $V_i = 200V$ ,  $V_o = 400V$  and  $P_o = 4kW$ )

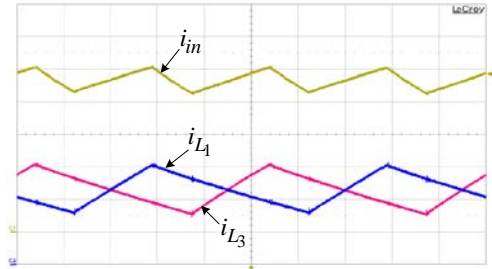


(a) Non-coupled inductor.

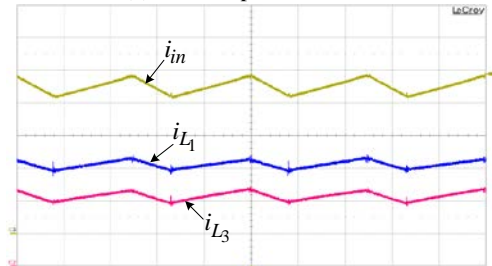


(b) Coupled inductor.

Fig. 13. Waveforms of the output filter inductor and output current. ( $[2A/div.]$ , [5 $\mu$ s/div.],  $V_i = 600V$ ,  $V_o = 400V$ ,  $P_o = 4kW$ )



(a) Non-coupled inductor.



(b) Coupled inductor.

Fig. 14. Waveforms of the qZ-source inductor and input current. ([5A/div.], [5 $\mu$ s/div.],  $V_i = 200V$ ,  $V_o = 400V$ ,  $P_o = 4kW$ )

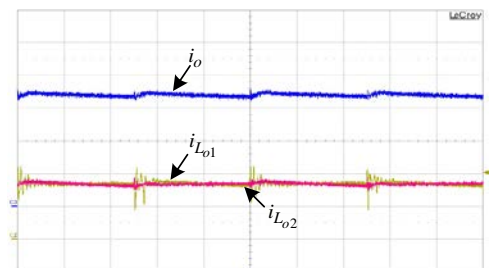
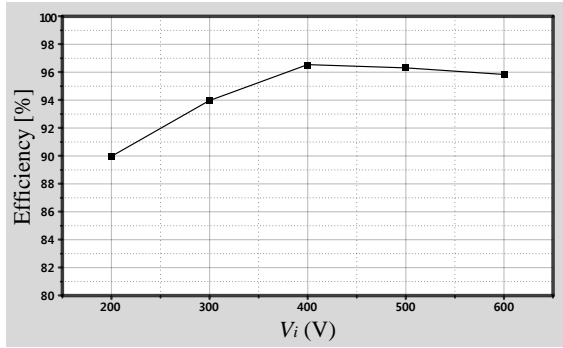
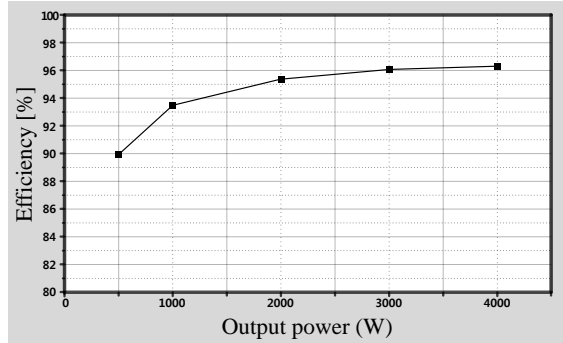


Fig. 15. Waveforms of the output filter inductor and output current. ( $[2A/div.]$ , [5 $\mu$ s/div.],  $V_i = 600V$ ,  $V_o = 300V$ ,  $D = 0.5$ )



(a) Efficiency vs. input voltage.



(b) Efficiency vs. output power.

Fig. 16. Efficiency of the proposed converter.

TABLE IV  
ELECTRICAL SPECIFICATIONS OF THE PROPOSED CONVERTER  
AND PARAMETERS OF THE COUPLED INDUCTORS

Output power	4 kW	
Input voltage range	200 $V_{dc}$ to 600 $V_{dc}$	
Output voltage	400 $V_{dc}$	
Switching frequency	20 kHz	
Z-source capacitors ( $C_1$ to $C_4$ )	50 $\mu$ F	
Coupled inductor in the qZ-source network	$L_1$ to $L_4$	210 $\mu$ H
	$k$	0.9
Coupled inductor in the output filter	$L_{o1}, L_{o2}$	210 $\mu$ H
	$k$	0.92

voltage and current waveforms of the proposed converter that operates in buck and boost modes. Given that the transformer current is a reflection of the output filter inductor current, the transformer current waveforms of the coupled case follow the inductor current waveforms.

Fig. 13 shows the current waveforms of the output filter inductor and output currents for both non-coupled and coupled cases. As expected, the inductor current ripples are reduced significantly with the coupled inductor while maintaining the same output current ripple. The two inductor currents are nearly in phase.

Fig. 14 shows the current waveforms of the qZ-source inductors and input currents for both non-coupled and coupled cases. The waveforms are similar to the case of output filter inductors.

Fig. 9 shows that the coupled inductor exhibits the smallest current ripple at  $D = 0.5$ , which can be identified in Fig. 15.

Fig. 16 shows the efficiency curves of the proposed converter that is measured as input voltage and output power vary.

## V. CONCLUSIONS

In this study presents a parallel operation of a qZ-source FB DC-DC converter that uses a coupled inductor. With this configuration, the proposed converter exhibits the following features.

- The converter has both buck and boost functions without increasing the switch voltage rating. The output voltage can be larger or smaller than the input voltage. Thus, the proposed converter has a desirable circuit topology when its input voltage range is wide.
- The converter can be short- and open-circuited without damaging switching devices. Therefore, it is resistant to EMI noise. Moreover, its robustness and reliability are significantly improved.
- The converter exhibits all the advantages of paralleling power converters such as modularity, ease of maintenance,  $(n+1)$  redundancy, and high reliability.

A 4 kW prototype that consist of two parallel qZ-source FB DC-DC converters is built and successfully tested to verify the operation principle of the proposed converter.

The converter is as valuable as a renewable energy source with a wide output range. It has an applied battery charge discharge system with a low current ripple. Extending power by parallel connection is also easy. By using buck and boost functions, reducing the current ripple, and realizing its parallel operation, the proposed converter can be a good candidate in such systems because requires wide input voltage variation and exhibits high reliability.

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## REFERENCES

- [1] F. Z. Peng, "Z-source inverter," *IEEE Trans. Ind. Appl.*, Vol. 39, No. 2, pp. 504-510, Mar./Apr. 2003.
- [2] J. Anderson and F. Z. Peng, "Four quasi-Z-Source inverters," in *Power Electronics Specialists Conference, (PESC) IEEE*, pp. 2743-2749, 2008.
- [3] F. Z. Peng, M. Shen, and Z. Qian, "Maximum boost control of the Z-source inverter," *IEEE Trans. Power Electron.*, Vol. 20, No. 4, pp. 833-838, Jul. 2005.
- [4] S. Ogasawara, J. Takagaki, H. Magi, and A. Nabae, "A novel control scheme of a parallel current-controlled PWM inverter," *IEEE Trans. Ind. Appl.*, Vol. 28, No. 5, pp.1023-1030, Sep. 1992.



- [5] K. Matsui, Y. Murai, M. Watanabe, M. Kaneko, and F. Ueda, "A pulsewidth-modulated inverter with parallel connected transistors using current-sharing reactors," *IEEE Trans. Power Electron.*, Vol. 8, No. 2, pp. 186-191, Apr. 1993.
- [6] F. Ueda, K. Matsui, M. Asao, and K. Tsuboi, "Parallel-connections of pulsewidth modulated inverters using current sharing reactors," *IEEE Trans. Power Electron.*, Vol. 10, No. 6, pp.673-679, Nov. 1995.
- [7] W. Pit-Leong, X. Peng, P. Yang, and F. C. Lee, "Performance improvements of interleaving VRMs with coupling inductors," *IEEE Trans. Power Electron.*, Vol. 16, No. 4, pp. 499-507, Jul. 2001.
- [8] L. Jieli, C. R. Sullivan, and A. Schultz, "Coupled-inductor design optimization for fast-response low-voltage," in *Applied Power Electronics Conference and Exposition (APEC 2002) Seventeenth Annual IEEE*, Vol. 2, pp. 817-823, 2002.
- [9] W. Pit-Leong, "Performance improvement of multi-channel interleaving VRM with integrated coupling inductors," Ph. D. Thesis, Virginia Polytechnic Institute and State University, USA, 2000.
- [10] L. Jieli, A. Stratakos, A. Schultz, and C. R. Sullivan, "Using coupled inductors to enhance transient performance of multi-phase buck converters," in *Applied Power Electronics Conference and Exposition, (APEC '04) Nineteenth Annual IEEE*, Vol. 2, pp. 1289-1293, 2004.
- [11] H. B. Shin, J. G. Park, S. K. Chung, H. W. Lee, and T. A. Lipo, "Generalised steady-state analysis of multiphase interleaved boost converter with coupled inductors," *Electric Power Applications, IEE Proceedings*, Vol. 152, pp. 584-594, 2005.
- [12] P. Zumel, O. Garcia, J. A. Cobos, and J. Uceda, "Magnetic integration for interleaved converters," in *Applied Power Electronics Conference and Exposition, (APEC'03) Eighteenth Annual IEEE*, Vol. 2, pp. 1143-1149, 2003.
- [13] L. Yim-Shu, W. Leung-Pong, and D. K. W. Cheng, "Simulation and design of integrated magnetics for power converters," *IEEE Trans. Magn.*, Vol. 39, No. 2, pp. 1008-1018, Mar. 2003.
- [14] A. F. Witulski, "Introduction to modeling of transformers and coupled inductors," *IEEE Trans. Power Electron.*, Vol. 10, No. 3, pp. 349-357, May 1995.



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