

A Cascaded D-STATCOM Integrated with a Distribution Transformer for Medium-voltage Reactive Power Compensation

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Abstract

This paper presents a novel integrated structure for a cascaded distribution static compensator (D-STATCOM) and distribution transformer for medium-voltage reactive power compensation. The cascaded multilevel converter is connected to a system via a group of special designed taps on the primary windings of the Dyn11 connection distribution transformer. The three-phase winding taps are symmetrically arranged and the connection point voltage can be decreased to half of the line-to-line voltage at most. Thus, the voltage stress for the D-STATCOM is reduced and a compromise between the voltage rating and the current rating can be achieved. The spare capacity of the distribution transformer can also be fully used. The working mechanism is explained in detail and a modified control strategy is proposed for reactive power compensation. Finally, both simulation and scaled-down prototype experimental results are provided to verify the feasibility and effectiveness of the proposed connection structure and control strategy.

Key words: Cascaded multilevel converter, Distribution static compensator (D-STATCOM), Distribution transformer, Reactive power compensation, Winding taps

I. INTRODUCTION

In recent years, distribution static synchronous compensators (D-STATCOMs) have become widely used in low voltage (LV) distribution systems [1], [2]. For high and medium voltage (HV and MV) applications, they are always connected to a grid through a step-down transformer due to the high voltage stresses of the switching devices. As a result, the custom coupling transformer makes the whole system bulky and costly. According to [3] the weight of a prototype D-STATCOM rated at 360-kVA is three tons and the transformer weighs about one ton (nearly one-third of the total weight). In order to reduce volume and weight, modular multilevel cascaded converters (MMCC) have been studied to realize transformer-less D-STATCOM [3]-[6].

The MMCC-based D-STATCOM can realize a high

blocking voltage and a low-harmonic output voltage, which makes it possible to eliminate the step-down transformer for medium-voltage (MV) applications. Among the MMCC family members, the single-star bridge cells (SSBC) topology seems to be the most suitable for D-STATCOMs. However, a reasonable compromise between the blocking voltage of the switching devices and the cascaded cells count must be achieved when it is applied to MV distribution systems (with nominal voltages of 10-kV in China, 6.6-kV in Japan or 13.8-kV in the United States). That is to say, if 3.3-kV insulated-gate bipolar transistors (IGBTs) are chosen, the cascaded cell count of per cluster may be four ($N=4$, taking a 6.6-kV transformer-less D-STATCOM as an example). Meanwhile, if 1.7-kV IGBTs are chosen, the cascaded count is eight ($N=8$) [7]. More cells in series will reduce the reliability and increase the control complexity. However, fewer cells make it necessary to choose high voltage switching devices. Although various manufactures (Infineon) have announced high power switching devices in ranges from 3.3-kV to 6.5-kV, these HV devices are more expensive and not always available. Their switching frequencies are limited to few hundred hertz [8]. In practice, the 1.7-kV and 1.2-kV IGBTs are the most cost

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effective power switches on the market.

To reduce the voltage stress across the power switch and dc-link capacitor, many hybrid combination structures for passive power filters (PPF) in parallel or series with STATCOMs have been proposed [8]-[11]. These devices are referred as hybrid STATCOMs (H-STATCOMs). Capacitors (C), thyristor switched capacitors (TSCs) or thyristor controlled reactors (TCRs) are connected in series to a voltage source converter (VSC) to indirectly reduce the connection voltage of the STATCOM, since most of the voltage drop occurs on the capacitors or passive branches. These kinds of topologies are helpful for reducing the rating of the dc link voltage of STATCOMs. However, the additional passive components may suffer from a resonance problem. In addition, the coordination control of the passive parts (C, TSC and TCR) and active parts is also a challenge.

Motivated by an autotransformer and considering the actual load characteristics of the existing distribution transformer, an integration structure of a STATCOM and distribution transformer was first proposed in [12], and it was called DT-STATCOM. That paper introduces the multi-group taps structure, and three STATCOMs are located at the vertices of the delta windings.

Unlike previous work, the main contribution of this paper is that a novel three-phase connection type for a CMC-based D-STATCOM and distribution transformer is designed and tested for voltage stress reduction and transformer capacity utilization improvement. The winding-taps (WT) on the primary windings are used for the connection of the VSC instead of the conventional point of common coupling (PCC). The system configuration, compensation mechanism and mathematical model are presented in detail. Considering the amplitude and phase-angle deviations of the voltages and currents between the primary side and the secondary side, a modified decoupled control scheme suitable for the new connection type is proposed. Both simulation and experimental results verify the performance of the proposed connection type for reactive power compensation.

II. SYSTEM CONFIGURATION AND MECHANISM ANALYSIS

A. Comparisons of Different Connection Points

D-STATCOM systems may be connected to a power grid in different ways for different requirements. The connection points are summarized in Fig. 1(a)-(c) [13], [14]. A D-STATCOM may be installed locally or at the consumer end, as shown in Fig. 1(a). The connection voltage of this configuration is relatively low and the compensation capacity of a single device is limited to several thousand Vars. This connection type is suitable for decentralized compensation. The drawback is that it is very costly and uneconomical to install a D-STATCOM wherever it is required because not all

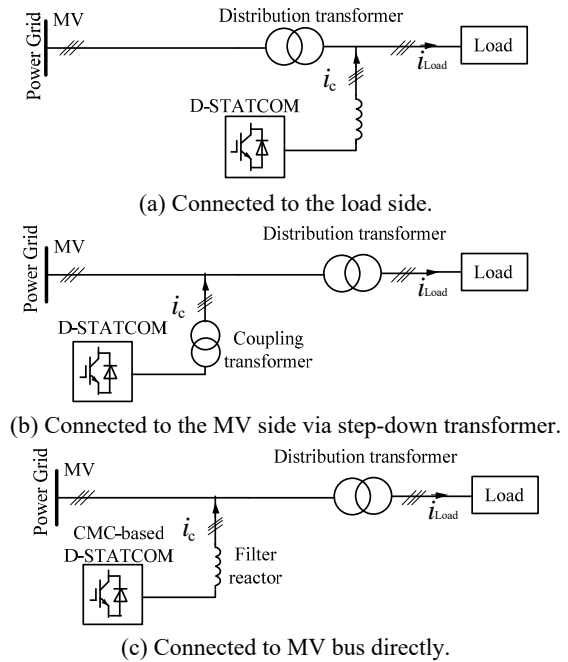


Fig. 1. Conventional connection alternatives for D-STATCOM system.

single device can share their compensation capacity with other devices [15]. To reduce the total installed capacity, the centralized configurations are developed as shown in Fig. 1(b) and (c). The compensation capacity of a single device may reach several megavar due to an increased connection voltage. The D-STATCOM is connected to MV buses via the specially designed coupling transformer shown in Fig. 1(b). The rated ac voltage of a CMC is determined by the coupling transformer secondary voltage and it can be chosen among the standard ac voltages specified in the IEC Standard Voltages [16]. As mentioned in Section I, a bulky coupling transformer makes it less attractive than the CMC-based transformer-less D-STATCOM shown in Fig. 1(c). In this case, the rated voltage of the CMC is the line voltage, and this relatively high voltage is handled by a set of cells together in each cluster. The coupling transformer is removed, but the cascaded cell count in series increases, which increases the complexity and unreliability. In addition, the installed capacity is always much greater than the actual demand. That is to say, it is not possible to make full use of the install capacity.

For the three connection types mentioned above, the D-STATCOM and the distribution transformer are considered in isolation. In fact, it is more flexible to use the distribution transformer to realize the changeable connection point voltage for the D-STATCOM. Therefore, the distribution transformer is used as the coupling transformer at the same time in this paper.

B. Proposed Connection Type

In previous research, the actual inductive and capacitive reactive power requirements as well as their daily and monthly

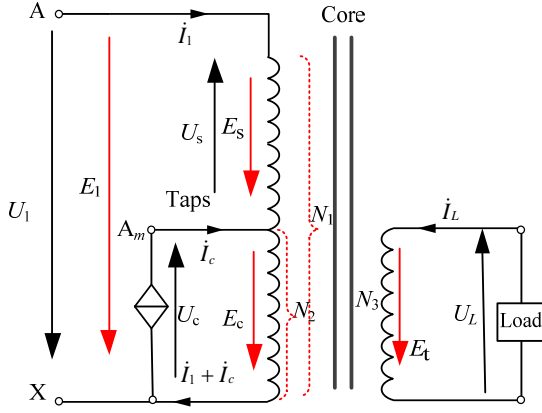


Fig. 4. Single-phase diagram of the proposed WTI D-STATCOM.

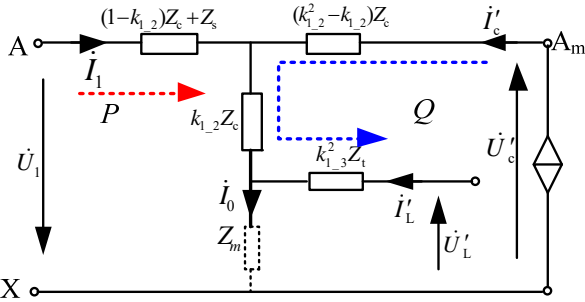


Fig. 5. Equivalent circuit of the proposed structure.

Applying *Kirchhoff's law*, it is possible get equations (5)-(8):

$$\dot{U}_s = \dot{E}_s - \dot{I}_1 Z_s \quad (5)$$

$$\dot{U}_c = \dot{E}_2 - (\dot{I}_1 + \dot{I}_c) Z_c \quad (6)$$

$$\dot{U}_L = \dot{E}_t - \dot{I}_L Z_t \quad (7)$$

$$\dot{U}_1 = -(\dot{U}_c + \dot{U}_s). \quad (8)$$

where Z_s , Z_c and Z_t are the leakage impedance of the series windings, common windings and tertiary windings, respectively. The leakage impedances can be obtained by short circuit tests [17].

In practice, the parameters are usually referred to the primary side (HV side in this case). The combination of Equ. (2) and (8) yields:

$$\dot{U}'_c = k_{12} \dot{U}_c = -\dot{I}_0 Z_m - (\dot{I}_0 - \frac{\dot{I}_L}{k_{13}}) k_{12} Z_c - (k_{12} - 1) Z_c \dot{I}_c \quad (9)$$

$$\dot{U}'_L = k_{13} \dot{U}_L = -\dot{I}_0 Z_m - k_{13} Z_t \dot{I}_L \quad (10)$$

$$\dot{U}_1 = \dot{I}_1 [(1 - k_{12}) Z_c + Z_s] + \dot{I}_0 Z_m + (\dot{I}_0 - \frac{\dot{I}_L}{k_{13}}) k_{12} Z_c \quad (11)$$

The equivalent circuit can be obtained from (9) to (11), as shown in Fig. 5. The compensation currents are injected via the taps. If the quantity of the output reactive power of the D-STATCOM is equal to that of the required load, the source only needs to apply active power. In this way, reactive power compensation can be achieved.

III. CONTROL STRATEGY

The control objectives are as follows. 1) Increasing the capacity utilization of the transformer to ensure its safety. 2) Controlling the CMC-based D-STATCOM to compensate the reactive current completely when the spare capacity of the distribution transformer is permitted. Therefore, the injected current must be well controlled to prevent the transformer from overloading. A control block diagram of the whole system is shown in Fig. 6. It mainly consists of three parts: the injection current calculation, decoupled current control and dc-link voltage control.

A. Injection Current Calculation

The load voltages and currents are detected to calculate the injection currents. The compensation currents are injected through the three-phase windings taps. The current detection point and the injection point are not the same node. There are amplitude and phase-angle deviations between the load currents and the injection currents due to the Dyn11 connection transformer. To clearly explain this, a phasor diagram is given in Fig. 7. The load voltage (secondary voltage) U_a (line-to-neutral) is 30° leading the primary side voltage U_{A0} (Dyn11 connection). If φ is the lagging power factor angle of the load before compensation, then the reactive current component can be expressed as:

$$i_{qt} = I_a \sin \varphi. \quad (12)$$

The D-STATCOM is connected to the winding taps labeled as A_1 , B_1 and C_1 . Therefore, the rated voltage of the star-configured CMC is U_{A10} , which can be expressed as (from Fig. 3):

$$U_{A10} = \sqrt{3\alpha^2 - 3\alpha + 1} U_{A0} = \sqrt{3\alpha^2 - 3\alpha + 1} k U_a \quad (13)$$

where k is the transformation ratio. Neglecting the active power absorbed by the CMC, the injected reactive current phasor I_{A1} is perpendicular to the connection point voltage U_{A10} . When the central taps are chosen as the connection taps, I_{A1} is in the same phase or the reversed phase as U_a .

The amplitude of I_{A1} can be calculated by applying the synchronous reference frame (SRF) to the three-phase load current. The phase angle of the load voltages $u_{la,b,c}$ is locked by a phase lock loop (PLL1). Then the reactive component of the load current can be obtained by:

$$\begin{bmatrix} i_{ldref} \\ i_{lqref} \\ i_0 \end{bmatrix} = \frac{2}{3} \begin{bmatrix} \sin \theta & \sin(\theta - 120^\circ) & \sin(\theta - 120^\circ) \\ \cos \theta & \cos(\theta - 120^\circ) & \cos(\theta + 120^\circ) \\ 0.5 & 0.5 & 0.5 \end{bmatrix} \begin{bmatrix} i_{la} \\ i_{lb} \\ i_{lc} \end{bmatrix} \quad (14)$$

The reactive component i_{lqref} cannot be used directly. It is transferred to the selective compensation block, which is designed to select complete compensation or partial compensation based on the following criteria:

$$I_{qlref} = \begin{cases} I_{qlref}, & \text{if } \beta \leq \beta_0 \\ \lambda I_{qlref}, & \text{if } \beta > \beta_0 \end{cases} \quad (15)$$

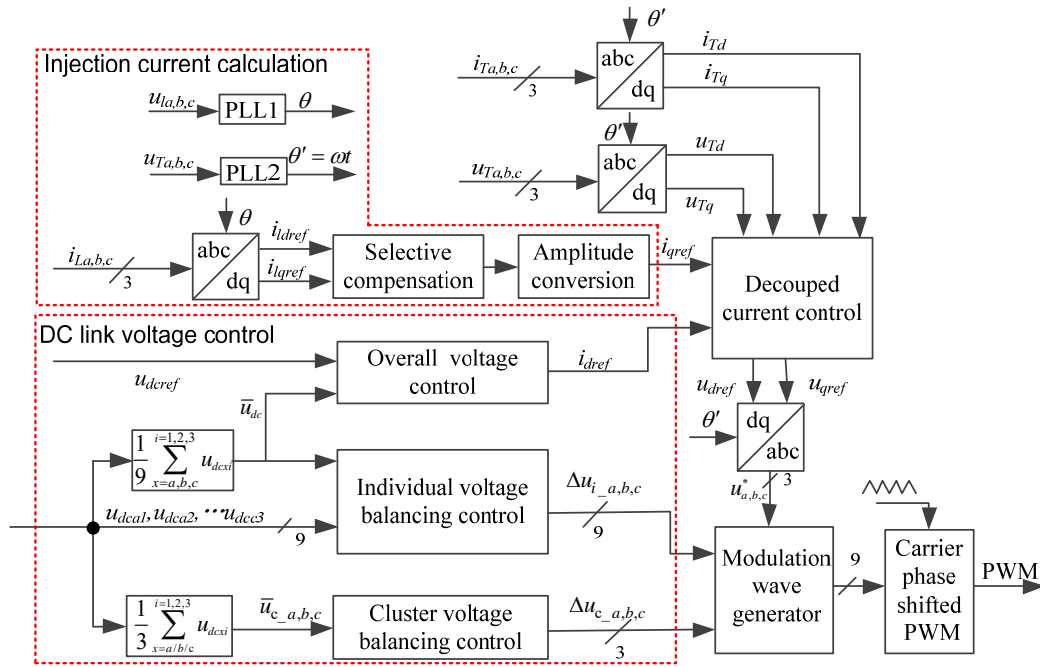


Fig. 6. Control block diagram of the prototype system.

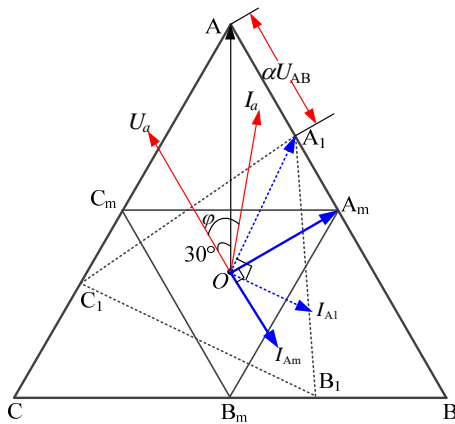


Fig. 7. Phasor diagram of the voltages and current across the transformer.

In (15), β is the actual load ratio of the distribution transformer, β_0 is the maximum allowable load ratio, and λ is the compensation coefficient ($0 < \lambda < 1$). The actual load ratio can be calculated via:

$$\beta = \frac{\sqrt{I_1^2 + I_2^2 + \dots + I_n^2 + i_{eq}^2}}{I_N} \quad (16)$$

where I_1, I_2, \dots, I_n are the RMS values of the fundamental, second harmonic, ..., n th harmonic components of the load currents (converted to the primary side), respectively; and I_N is the rated current of the transformer on the primary side. i_{eq} is the approximate equivalent current, which represents the losses (including no-load losses and load losses) of the distribution transformer itself. Generally, it is not easy to accurately measure the total losses of the transformer, because the load losses are variable under different load

conditions. Thus, the corresponding equivalent current i_{eq} cannot be obtained easily. Considering that these losses are only a small part of the power rating of the transformer (usually less than 5%), i_{eq} is set to $0.05 I_N$ to simplify the calculation.

The maximum allowable load ratio β_0 is determined by the rated current of the primary windings. To prevent the windings from overcurrent after current injection, the following constraint must be satisfied:

$$\max \{i_{AAm}, i_{AmX}\} \leq I_N \quad (17)$$

where i_{AAm} and i_{AmX} are the currents of the series winding and the common winding in phase a , respectively. In general, it is not easy to directly measure the winding currents. From Fig. 4, the proposed connection type is similar to an autotransformer. According to autotransformer theory, the capacities of the series winding and the common winding are equal, i.e. $S_{AAm} = S_{AmX}$. Assuming that the apparent power of a two-winding transformer is S_N , it is possible to obtain:

$$S_{AAm} = S_{AmX} = S_N \left(1 - \frac{1}{K_A}\right) \quad (18)$$

where K_A is the voltage ratio and $K_A = N_1/N_2$. In this paper, the central taps are chosen as the connection points. Thus, it is possible to obtain $K_A = 2$ and $S_{AAm} = S_{AmX} = 0.5 S_N$. Considering the safety margin, the maximum allowable load ratio β_0 is set as 0.4 according to field tests, and verified by experiments.

If the actual load ratio is less than β_0 , the reactive current is completely compensated. Meanwhile, if β is greater than β_0 , complete compensation may cause an overcurrent of the transformer windings (shown in the Appendix).

The output of the selective compensation block should be converted to the current injection point based on the reactive power balance principle by the amplitude conversion block. This can be expressed as:

$$I_{qref} = \frac{I_{qref} \cdot U_a}{U_{A10}} = \frac{I_{qref}}{k\sqrt{3\alpha^2 - 3\alpha + 1}}. \quad (19)$$

B. Decoupled Current Control

Conventional PI controllers are applied for inner loop current control. The difference is that the connection point of the D-STATCOM is the winding taps instead of the PCC. The phase angle θ' of the taps voltages (subscript T) is locked by PLL2. The d -axis and q -axis components of u_{Tabc} and i_{Tabc} can be obtained by applying a d - q transformation. The controller can be designed as:

$$\frac{d}{dt} \begin{bmatrix} i_{Td} \\ i_{Tq} \end{bmatrix} = L \begin{bmatrix} K_p(i_{dref} - i_{Td}) + K_i \int (i_{dref} - i_{Td}) dt \\ K_p(i_{qref} - i_{Tq}) + K_i \int (i_{qref} - i_{Tq}) dt \end{bmatrix} \quad (20)$$

where $L=L_1+L_2$ is the equivalent inductance of the LCL filter, and K_p and K_i are the proportional gain and integral gain, respectively.

C. DC Link Voltage Balancing Control

For the CMC-based transformer-less D-STATCOM, it is essential to balance the dc capacitor voltage. Many dc capacitor voltage balancing control methods have been presented for the CMC-based STATCOM [18], [19]. The voltage balancing control method proposed in [6] and [19] is applied here. This voltage balance control consists of three layers: overall voltage control, cluster voltage balancing control (CVBC) and individual voltage balancing control (IVBC). In Fig. 2, the reference dc link voltage of each cell is $u_{dc_{ref}}$. The dc capacitor voltage of each cell is $u_{dc_{xi}}$ ($x=a,b,c; i=1,2,\dots,n$). The dc mean voltage of all of the cells is given by:

$$\bar{u}_{dc} = \frac{1}{3n} \sum_{x=a}^c \sum_{i=1}^n u_{dc_{xi}}. \quad (21)$$

The dc mean voltage of one cluster is given by:

$$\bar{u}_{dc_x} = \frac{1}{n} \sum_{i=1}^n u_{dc_{xi}} \quad (x=a,b,c). \quad (22)$$

The overall voltage balancing control is designed to achieve \bar{u}_{dc} approaching $u_{dc_{ref}}$ ($\bar{u}_{dc} \rightarrow u_{dc_{ref}}$). The cluster CVBC is designed to achieve \bar{u}_{dc_x} approaching \bar{u}_{dc} ($\bar{u}_{dc_x} \rightarrow \bar{u}_{dc}$), while the IVBC is designed to achieve $u_{dc_{xi}}$ approaching \bar{u}_{dc_x} ($u_{dc_{xi}} \rightarrow \bar{u}_{dc_x}$).

The overall voltage control considers a set of three clusters as a three-phase full-bridge converter using six IGBTs. This can be achieved by adjusting the d -axis current command i_{dref} . The controller can be expressed as:

$$i_{dref} = K_{p_udc}(u_{dc_{ref}} - \bar{u}_{dc}) + K_{i_udc} \int (u_{dc_{ref}} - \bar{u}_{dc}) dt \quad (23)$$

where K_{p_udc} and K_{i_udc} are the proportional and integral gain,

respectively.

A similar method, presented in [19], is applied for the CVBC and the IVBC. A visualized explanation for the a -phase i th cell is shown in Fig. 8. The idea is that two voltage vectors u_{c_a} and $u_{m_{ai}}$ are added to the reference voltage u_{aref} to produce a minor pulse width adjustment. This adjustment can change the active power absorbed by the H-bridge cell. The procedures for designing appropriate voltage vectors are given in the following part.

The minor voltage vector u_{c_x} for the CVBC (being in phase or out of phase by 180° with the ac output signal u_{xref} from the decoupled current control) is superimposed to the output voltage of each cluster. The voltage vectors can be designed as:

$$\begin{bmatrix} u_{c_a} \\ u_{c_b} \\ u_{c_c} \end{bmatrix} = K_c S \begin{bmatrix} \bar{u}_{dc} - \bar{u}_{dc_a} \\ \bar{u}_{dc} - \bar{u}_{dc_b} \\ \bar{u}_{dc} - \bar{u}_{dc_c} \end{bmatrix} \quad (24)$$

where K_c is the proportional gain and the matrix $S = \text{diag}\{\sin\theta', \sin(\theta' - 120^\circ), \sin(\theta' + 120^\circ)\}$. Detailed step-by-step procedures for designing the gain parameters are given in [19]. The control block diagram is redrawn in Fig. 9.

The voltage vector of the a -phase is given by:

$$u_{c_a} = K_3 [K_2 (\bar{u}_{dc} - \bar{u}_{dc_a}) - i_d] \sin(\omega t) \quad (25)$$

In the steady state, the dc mean voltage for all of the cells \bar{u}_{dc} is very close to its reference value $u_{dc_{ref}}$ (it has a margin of error of plus or minus 2 percent). Therefore, the output of the outer-loop PI controller (i_{dref}) is relatively small according to equation (23). The active current component i_d in (25) should track the d -axis current reference i_{dref} obtained from (23). Generally, the response speed of the inner current loop is much faster than that of the outer voltage loop. As a result, i_d tracks i_{dref} in a short time. Hence, i_d is relatively small in the steady state (only balancing the power losses of the converter).

On the other hand, the CVBC is the second layer dc-link voltage balancing control. It is realized on the foundation of the overall voltage control (first layer). The system can be regarded as being approximately in the steady state when designing the CVBC. Therefore, the active current component i_d in (25) can be neglected. Finally, the superposed voltage vector for the CVBC can be expressed as:

$$u_{c_a} = K_3 [K_2 (\bar{u}_{dc} - \bar{u}_{dc_a})] \sin(\omega t) \quad (26)$$

Therefore, the gain K_c is set to $K_2 K_3$ in the proposed design. To select appropriate values for K_2 and K_3 , the inner-loop transfer function G according to Fig. 9 is written as:

$$G = \frac{K_3 K_{PWM}}{sL_{AC} + K_3 K_{PWM}} \quad (27)$$

The converter gain $K_{PWM} = 1$, is a first-order system with a time constant of:

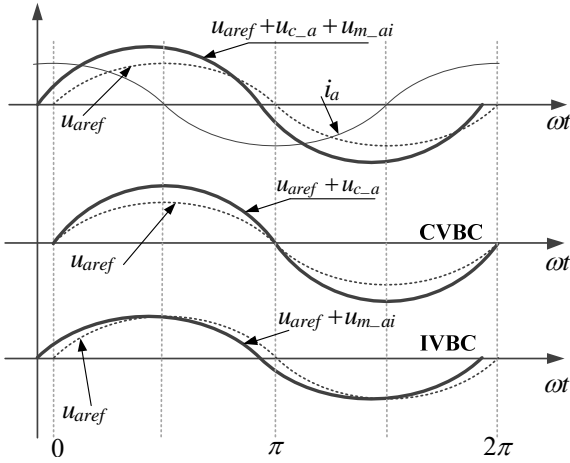


Fig. 8. Visualized explanation of the CVBS and IVBS for a -phase i th cell.

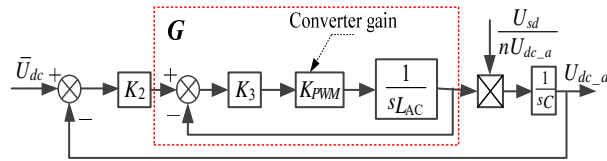


Fig. 9. Control block diagram of the CVBS for a -phase.

$$\tau_c = \frac{L_{AC}}{K_3 K_{PWM}} \approx \frac{L_{AC}}{K_3} \quad (28)$$

In general, the time constant is set to a fundamental period ($\tau_c = 20ms$), so the first parameter K_3 can be obtained from $K_3 = L_{AC}/\tau_c$. Similarly, the overall transfer function can be expressed as:

$$\frac{U_{dc}}{\bar{U}_{dc}} = \frac{K_2 K_3 U_{sd}}{(CL_{AC} n U_{dc_a}) s^2 + (CK_3 n U_{dc_a}) s + K_2 K_3 U_{sd}} \quad (29)$$

It is a typical second-order system and the damping ratio is given by:

$$\zeta = \frac{1}{2} \sqrt{\frac{nCK_3 U_{dc_a}}{K_2 U_{sd} L_{AC}}} \quad (30)$$

According to control theory, ζ is selected as the optimal damping ratio, i. e. $\zeta = 0.707$. Then it is possible to obtain an appropriate K_3 from (30).

Taking phase a as an example to explain the IVBC, the D-STATCOM mainly draws reactive power from the grid in the steady state ($\dot{i}_{Tq} \gg \dot{i}_{Td}$). Therefore, the current flowing in phase a can be expressed as:

$$i_a = I_q \cos(\omega t) \quad (31)$$

To produce an amount of active power for each capacitor voltage balancing, the idea is superimposing a voltage vector in the same phase or reversed phase as i_a to the reference voltage of the corresponding cell. Therefore, it is possible to design the following voltage vector as:

$$\begin{aligned} u_{m_ai} &= K_{i_udc} (\bar{u}_{dca} - u_{dc_ai}) \cos(\omega t) \\ &= K_{i_udc} \Delta u_{dc_ai} \cos(\omega t) \end{aligned} \quad (32)$$

The sign of the gain K_{i_udc} depends on the operation mode of the D-STATCOM. It is positive for the inductive mode and negative for the capacitive mode. The controller gain K_{i_udc} is designed considering two aspects. One is the operation range of the D-STATCOM, and the other is the response speed.

1) *Operation Range*. As mentioned above, the active power for the IVBC is influenced by the q -axis current command i_{qref} (reactive current command). This means that the IVBC does not work when $i_{qref} = 0$. The produced active power is different for different values of i_{qref} corresponding to different operation conditions under the same controller gain K_{i_udc} . If the D-STATCOM is working close to its rated capacity (i_{qref} is close to its upper limit i_{max}), too large a gain may cause instability. Therefore, a tradeoff is made to adapt to different values of i_{qref} and it is assumed that the D-STATCOM is working under the half load condition ($i_{qref} = \pm 0.5i_{qmax}$) to design the gain.

2) *Response Speed*. The IVBC is designed to be slower in response speed than the cluster balancing control to avoid a conflict of control between the two. This has been verified in [6] by experimental results. Therefore, the IVBC is regarded as a voltage controller in the proposed design. A detailed derivation is given as follows:

The active power for the dc-voltage of the a -phase i th cell can be obtained from equations (31) and (32):

$$P_{ai} = u_{dcai} \cdot i_{dcai} = (\bar{u}_{dca} - \Delta u_{dcai}) \cdot C \frac{d(\bar{u}_{dca} - \Delta u_{dcai})}{dt} \quad (33)$$

As mentioned above, the IVBC is slower in response time than the cluster balancing control. Therefore, the dc mean voltage \bar{u}_{c_a} in (33) can be regarded as a constant. Considering $\bar{u}_{c_a} \gg \Delta u_{dcai}$, (33) can be expressed as:

$$P_{ai} = -C \bar{u}_{dca} \frac{d\Delta u_{dcai}}{dt} \quad (34)$$

From equation (31)-(34), the control block diagram of the IVBC can be obtained, as shown in Fig. 10.

The closed loop transfer function is given by:

$$\frac{\Delta U_{dcai}(s)}{D_{ai}(s)} = \frac{1}{sC\bar{U}_{dca} + K_{i_udc} I_q} \quad (35)$$

The time constant of the transfer function is given by:

$$\tau = \frac{C\bar{U}_{dca}}{K_{i_udc} I_q} \quad (36)$$

The time constant can be chosen according to on-site tuning as long as it can guarantee that the response speed is slower than the cluster balancing control (in [6], it was set to 5s). Once the time constant is determined ($\tau = \tau_0$), the controller gain K_{i_udc} can also be determined from (36), i. e.:

$$K_{i_udc} = \frac{C\bar{U}_{dca}}{\tau_0 I_q} \quad (37)$$

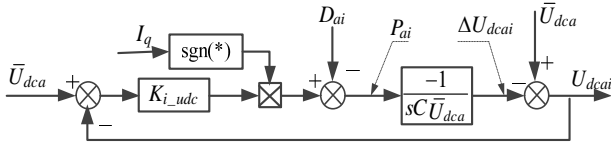


Fig. 10. Control block diagram of the IVBC for a -phase i th cell.

Consider the operation range mentioned above, I_q is set to $0.5I_{q\max}$ as a compromise.

IV. SIMULATION ANALYSIS AND EXPERIMENTAL VERIFICATION

A. Simulation Analysis

To verify the feasibility and effectiveness of the proposed connection type and its performance, simulations are performed in the Matlab/Simulink environment. The specially designed multi-taps distribution transformer is modeled by three single phase multi-winding transformers provided by SimPowerSystem. To simplify the verification, a 9-level CMC-based D-STATCOM with a LCL filter is connected to the central taps of the distribution transformer for compensation currents injection. The simulation parameters are listed in Table I.

At first, the load is set to $200\text{kW}+j200\text{kVar}$ (inductive). The load ratio of the transformer is about 0.3. Fig. 11 (a)-(d) shows simulation results when the STATCOM is working in the inductive mode. At $t=0.1\text{s}$, the STATCOM is switched on. An orthogonal compensation current (u_{TA} is leading i_{TA} by 90 degrees) is injected via the winding taps. After compensation, the source current is completely in phase with the source voltage in less than half of a power cycle. Meanwhile, the source current is significantly reduced. The connection point voltage of the D-STATCOM is only half of the line voltage.

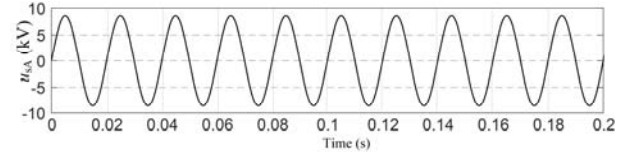
To test the dynamic performance of the proposed structure, a sudden load change from inductive to capacitive ($-j200\text{kVar}$) is made at $t=0.2\text{s}$. Fig. 12 (a) and (b) show simulation results in this case. When the load is changed, the source current is always in phase with the source voltage. However, the phase of the injection current changes from lagging to leading. It can also be observed that the D-STATCOM obtains a good dynamic compensation performance. The response time is less than half of a fundamental cycle.

B. Experimental Verification

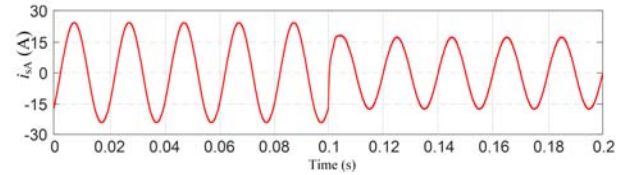
To further validate the proposed connection type, a down-scaled experimental prototype is developed in the laboratory. The prototype comprises a specially designed dry-type transformer with a rated capacity of 50-kVA and a CMC-based 7-level converter. The transformer is shown in Fig. 13. Each phase has five winding taps labeled X_1 to X_5 ($X=A, B$ and C) on the primary windings. The position of the phase-A taps is shown on the left of Fig. 13. The rated voltage of the primary winding is 800V. A_3 is a central tap and the voltage is

TABLE I
SIMULATION PARAMETERS

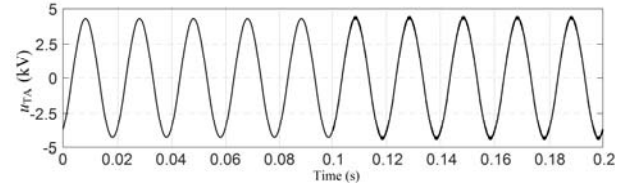
Parameter	Value
Source voltage	10kV rms line to line, 50Hz
Distribution transformer	1MVA, 10kV/380V, Dyn11
Switching frequency	2-kHz
LCL filter	$L_1=2\text{mH}, L_2=0.6\text{mH}, C=15\ \mu\text{F},$ $R_d=5\ \Omega$
DC link capacitor	$C_{dc}=6000\ \mu\text{F}$
DC link voltage	1500V



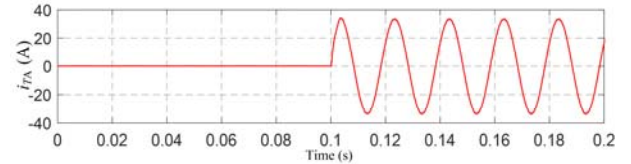
(a) Source voltage u_{sA} (line-to-neutral).



(b) Source current i_{sA} .

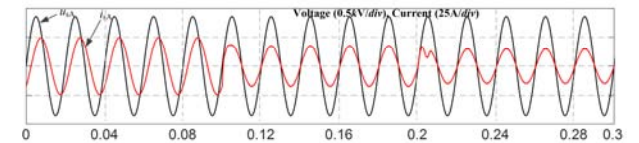


(c) Connection point voltage u_{TA} .

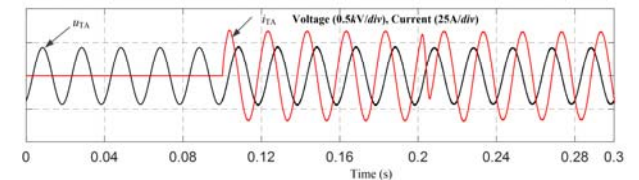


(d) Injection current i_{TA} .

Fig. 11. Simulation results of proposed structure working in inductive mode.



(a) Source voltage and current.



(b) Connection point voltage and injection current.

Fig. 12. Simulation results in a transient state from inductive operation to capacitive operation.

TABLE II
PARAMETERS OF THE EXPERIMENTAL SYSTEM

Parameters	Value
Source voltage	800V,rms line-to-line, 50Hz
System impedance*	12 Ω
Distribution transformer	50kVA, 800/380V, Dyn11
Switching frequency	2kHz
LCL filter	$L_1=1\text{mH}$, $L_2=0.2\text{mH}$, $C=10\ \mu\text{F}$, $R_d=2\ \Omega$
Maximum output current of APF	25A (Amplitude)
Cascaded count	3

*Large system impedance is employed according to the safety codes of the authors' lab.

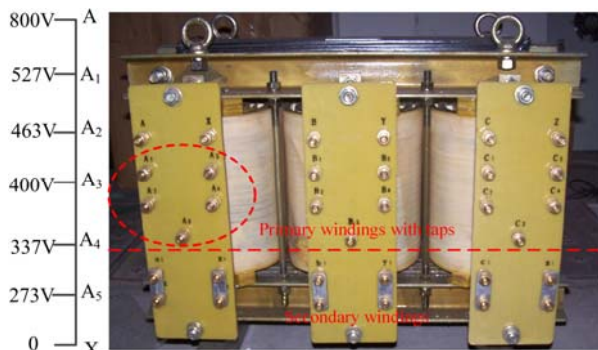


Fig. 13. Image of the three-phase transformer with several taps.

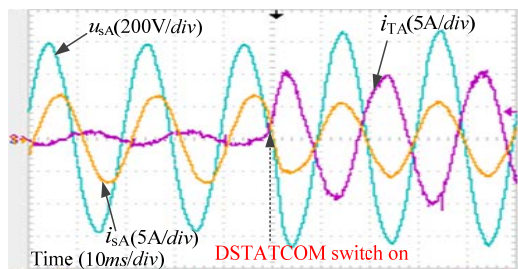
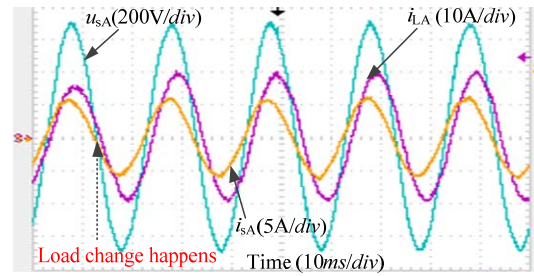


Fig. 14. Experimental waveforms show dynamic performance of the proposed structure.

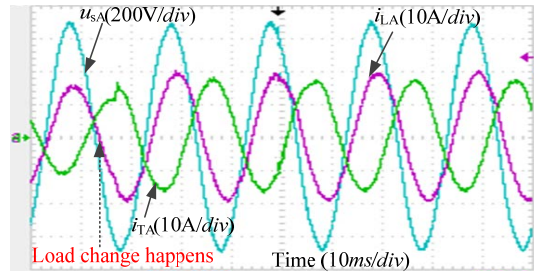
400V. The other taps are placed symmetrically. To simulate a reactive load, an active power filter (APF) with a rated current of 25A is adopted to generate reactive power and it is configured in parallel with the active load. The other parameters are shown in Table II.

At first, the active load is set to 4.8-kW and the APF is controlled to generate a 10A (amplitude) reactive current to simulate a 4.6-kVar reactive load (inductive). Therefore, the power factor (PF) of the load side can be easily calculated as 0.72. The actual load ratio of the transformer is about 0.15.

Fig. 14 shows experimental results and u_{sA} , i_{sA} and i_{TA} are the source voltage (line-to-neutral), source current and injection current of phase-A, respectively. When the D-STATCOM is switched on, the compensation current i_{TA} is injected through the winding taps. The voltage and current of

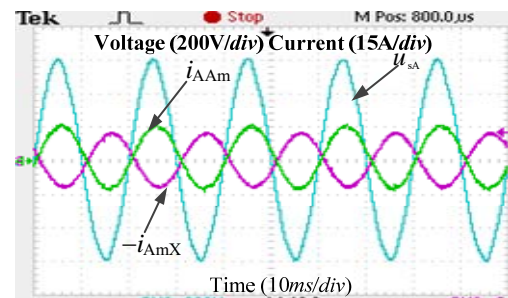


(a) Source voltage u_{sA} , source current i_{sA} and load current i_{LA} .

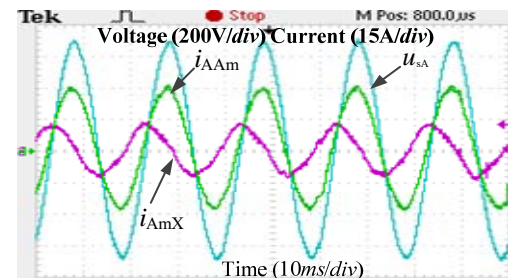


(b) Injected current i_{TA} .

Fig. 15. Experimental results under load sudden change.



(a) Before compensation.



(b) After complete compensation.

Fig. 16. Waveforms of the winding currents in phase a under inductive operation mode.

the 800V source are almost in phase with each other after about half of a power cycle (10ms). The PF of the source side is increased to unity power factor.

The reactive current is compensated effectively with the winding taps injection method. From Fig. 14, it can be seen that the primary side voltage of the distribution transformer is also increased due to the reduced line drop.

In the following test, the initial reactive current generated by the APF is set to 10A (amplitude, capacitive) and increased to 15A suddenly to simulate the sudden load change condition. The load ratio of the transformer is

increased to 0.23.

The selective compensation and amplitude conversion blocks shown in Fig. 10 are designed to prevent the transformer from overloading after compensation current injection. The threshold value of β_0 is set to 0.2, i.e. if the actual load ratio of the distribution transformer is greater than 0.2, the actual injection currents are limited to its upper limit. In this case, the reactive current is not compensated completely. The source voltage u_{sA} and current i_{sA} are not exactly in phase, as shown in Fig. 15(a). Once a load change occurs (the load current of phase-A i_{LA} increases), the D-STATCOM can increase and inject its output current via the winding taps in less than 10ms, as shown in Fig. 15(a) and (b). The PF is increased from 0.38 to 0.9. The results demonstrate the effectiveness of the proposed connection type for reactive power compensation.

V. CONCLUSIONS

In this paper, a novel connection type for the D-STATCOM and distribution transformer is proposed for medium voltage reactive power compensation. The VSC is connected to the three-phase symmetrical winding taps on the primary windings of the transformer for compensation current injection. This way the distribution transformer is used as a coupling transformer of the CMC-based D-STATCOM. The spare capacity of the transformer can be fully used. Both simulation and experimental test results validate the effectiveness of the winding taps injection method.

APPENDIX

The maximum allowable load ratio β_0 is tested in the lab. Fig. 16 shows waveforms of the winding currents before and after compensation. The currents are measured by Tek-A622 current probes. Before compensation, the load ratio is set to 0.45 and the currents of the two winding segments AAm and AmX are almost the same (Note that i_{AmX} is in the reverse phase to show the two curves clearly), as shown in Fig. 16(a). After complete compensation, the currents of the two segments are not equal and i_{AAm} is greater than i_{AmX} , which is similar to the operation characteristic of the autotransformer. The load ratio is greater than β_0 (0.4) and i_{AAm} is close to the rated current of the windings (30A), which verifies the analysis in section III-A.

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