

Discharging/Charging Voltage-Temperature Pattern Recognition for Improved SOC/Capacity Estimation and SOH Prediction at Various Temperatures

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Abstract

This study investigates an application of the Hamming network-dual extended Kalman filter (DEKF) based on pattern recognition for high accuracy state-of-charge (SOC)/capacity estimation and state-of-health (SOH) prediction at various temperatures. The averaged nine discharging/charging voltage-temperature (DCVT) patterns for ten fresh Li-Ion cells at experimental temperatures are measured as representative patterns, together with cell model parameters. Through statistical analysis, the Hamming network is applied to identify the representative pattern that matches most closely with the pattern of an arbitrary cell measured at any temperature. Based on temperature-checking process, model parameters for a representative DCVT pattern can then be applied to estimate SOC/capacity and to predict SOH of an arbitrary cell using the DEKF. This avoids the need for repeated parameter measurement.

Key Words: Dual extended Kalman filter (DEKF), Hamming network, Pattern recognition, State-of-charge (SOC), State-of-health (SOH)

I. INTRODUCTION

A battery management system (BMS) is critical for maintaining cell performance [1], [2]. Specifically, in electric vehicles (EV) and hybrid electric vehicles (HEV) applications, it is important to have information about the cell's state-of-charge (SOC) [3], [4], capacity [5], [6], and state-of-health (SOH) based on the pulse power analysis [7], [8]. Precise SOC, capacity, and SOH information are critical in practical applications where it is necessary to determine how long the cell will last and, importantly, when to stop charging and discharging, as over-charging and over-discharging may cause permanent internal damage [9].

In recent years, much research has been devoted to developing improved methods for SOC/capacity estimation and SOH prediction. Specifically, the dual extended Kalman filter (DEKF), which makes use of two extended Kalman filters (EKFs) running in parallel, is widely used for state and parameter estimations [10]. The accuracy of the DEKF depends largely on the predetermined parameter values used in the equivalent circuit model shown in Fig. 1. It is important to accurately measure model parameters that include open circuit voltage (OCV), series resistance (R_i), diffusion

resistance (R_{Diff}), and diffusion capacitance (C_{Diff}) [11], [12]. Because these parameters vary with temperature, the accuracy of SOC/capacity estimation and SOH prediction method will also vary. The error can be reduced by repeating parameter measurement, but such an exercise would be very time-consuming and inefficient. Thus, the existing DEKF algorithm can only be applied to a single cell under controlled experimental conditions [11], [12]. It should be considered that the model parameters could be changed for correct SOC/capacity estimation and SOH prediction.

This study proposes an application of the DEKF that performs high accuracy SOC/capacity estimation and SOH prediction based on pattern recognition at various temperatures. In this study, the Hamming network [13], [14] generally used for binary pattern recognition is utilized to evaluate several predetermined representative discharging/charging voltage-temperature (DCVT) patterns, and determine which is the closest to the input DCVT pattern by comparing the inner product. Representative DCVT patterns are collected from nine average discharging/charging voltages from ten Li-Ion cells. Based on the temperature-checking process, the model parameters determined from the representative DCVT pattern are then applied to estimate SOC/capacity and predict SOH of an arbitrary cell using the DEKF. This avoids the need for repeated parameter measurement. Experimental studies were conducted on Samsung 18650 Li-Ion cells with a total capacity of 1.3Ah.

Manuscript received Jul. 31, 2011; revised Oct. 3, 2011

Recommended for publication by Associate Editor Woo-Jin Choi.

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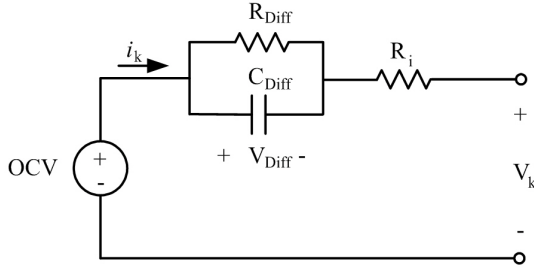


Fig. 1. Lumped parameter cell model: open circuit voltage (OCV), series resistance (R_i), diffusion resistance (R_{Diff}), and diffusion capacitance (C_{Diff}).

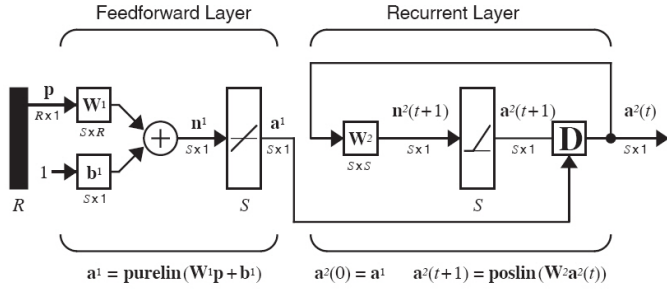


Fig. 2. Hamming network.

II. HAMMING NETWORK

The Hamming network [13], [14] is used for pattern recognition, as shown in Fig. 2. It is one of the simplest competitive networks and is designed explicitly to solve binary pattern recognition issues. The Hamming network decides which representative pattern is closest to the current pattern by comparing the inner products. Its objective is to decide which prototype vector is closest to the input vector. The Hamming network consists of two layers: the feedforward layer and the recurrent layer.

A. Feedforward Layer

The feedforward layer performs a correlation or inner product between each prototype pattern and a current pattern in order to search a minimum Hamming distance (HD) from calculation the difference between dimension m and HD. In order to perform the inner product, weight matrix \mathbf{W}^1 is set to prototype vectors and is transformed into the binary form, in addition to bias vector, and is transformed into the binary form, in addition to bias vector, \mathbf{b}^1 , in (1) and (2).

$$\mathbf{W}^1 = \begin{bmatrix} 1\mathbf{w}^{iT} \\ 2\mathbf{w}^{iT} \\ \vdots \\ S\mathbf{w}^{iT} \end{bmatrix} = \frac{1}{2} \begin{bmatrix} 1\mathbf{w}^1 & 2\mathbf{w}^1 & \cdots & S\mathbf{w}^1 \\ 1\mathbf{w}^2 & 2\mathbf{w}^2 & \cdots & S\mathbf{w}^2 \\ \vdots & \vdots & \ddots & \vdots \\ 1\mathbf{w}^R & 2\mathbf{w}^R & \cdots & S\mathbf{w}^R \end{bmatrix} = \begin{bmatrix} \mathbf{p}_1^T \\ \mathbf{p}_2^T \\ \vdots \\ \mathbf{p}_S^T \end{bmatrix} \quad (1)$$

$$\mathbf{b}^1 = [R, R, \dots, R]^T = \left[\frac{m}{2}, \frac{m}{2}, \dots, \frac{m}{2} \right]^T \quad (2)$$

where each row of \mathbf{W}^1 represents a prototype vector which it is required to be recognize, and each element of \mathbf{b}^1 , $m/2$ is the threshold value and is set equal to the number of elements in each input vector R , S is the number of neurons. As expressed in (3), it is high desirable to have the i^{th} ($1 \leq i \leq R$) node in this layer compute $m - HD(i, \mathbf{w}, \mathbf{p})$ for a given input vector \mathbf{p} ,

where $HD(i, \mathbf{w}, \mathbf{p})$ is the Hamming distance between vectors $i\mathbf{w}$ and \mathbf{p} . Then, the net input of node is as in (4), namely, the feedforward layer output. After all, these outputs are equal to the inner products of the prototype vectors with the input, plus R . In the feedforward layer, a linear transfer function (**purelin**) [14] is used to implement these inner products in (5). The neuron in this layer with the largest output corresponds to the prototype pattern that is closest in Hamming distance to the input pattern.

$$\mathbf{W}^1\mathbf{p} = [m - HD(i, \mathbf{w}, \mathbf{p})] - HD(i, \mathbf{w}, \mathbf{p}) \quad (3)$$

$$\text{net}_i = \mathbf{n}^1 = \mathbf{W}^1\mathbf{p} + \frac{m}{2} = m - HD(i, \mathbf{w}, \mathbf{p}) \quad i = 1, 2, \dots, S \quad (4)$$

$$\mathbf{a}^1 = \mathbf{W}^1\mathbf{p} + \mathbf{b}^1 = \begin{bmatrix} \mathbf{p}_1^T\mathbf{p} + R \\ \mathbf{p}_2^T\mathbf{p} + R \\ \vdots \\ \mathbf{p}_S^T\mathbf{p} + R \end{bmatrix} = \text{purelin}(\mathbf{W}^1\mathbf{p} + \mathbf{b}^1). \quad (5)$$

B. Recurrent Layer

The recurrent layer is known as the MAXNET [14] and a competitive layer which performs the winner-take-all (WTA) operation, whose purpose is to enhance the initial dominant response of the i^{th} node and suppress the others [14]. As expressed in (6), the neurons are initialized with the outputs of the feedforward layer, which indicate the correlation between the prototype vectors and the input vector.

$$\mathbf{a}^2(0) = \mathbf{a}^1. \quad (6)$$

As a result of recurrent processing, the i^{th} node responds positively while the responses of all remaining nodes decay to zero. Namely, in order to determine a winner, which only has a positive output, the neurons compete with each other. Then, as expressed in (7), the recurrent layer output is updated according to the following recurrence relation which uses a positive transfer function [14].

$$\mathbf{a}^2(t+1) = \text{poslin}(\mathbf{W}^2\mathbf{a}^2(t)). \quad (7)$$

This processing requires self-feedback connections and negative lateral inhibition connections, in which the output of each neuron has an inhibitory effect on all of the other neurons [14]. The $n \times n$ weight matrix of the recurrent layer \mathbf{W}^2 is taken in (8). The weights in this layer are set so that the diagonal elements are 1, and the off-diagonal elements have a small negative value, where $0 < \varepsilon < 1/(S-1)$ is called the lateral interaction coefficient. Thus, it can be substituted weight values of 1 and $-\varepsilon$ for the appropriate elements of \mathbf{W}^2 in (9), where $1 \leq i \leq S$ and $1 \leq j \leq S$.

$$\mathbf{W}^2 = \begin{bmatrix} 1\mathbf{w}^1 & 1\mathbf{w}^2 & \cdots & 1\mathbf{w}^S \\ 2\mathbf{w}^1 & 2\mathbf{w}^2 & \cdots & 2\mathbf{w}^S \\ \vdots & \vdots & \ddots & \vdots \\ i\mathbf{w}^1 & i\mathbf{w}^2 & \cdots & i\mathbf{w}^S \end{bmatrix} = \begin{bmatrix} 1 & -\varepsilon & \cdots & -\varepsilon \\ -\varepsilon & 1 & \cdots & -\varepsilon \\ \vdots & \vdots & \ddots & \vdots \\ -\varepsilon & -\varepsilon & \cdots & 1 \end{bmatrix} \quad (8)$$

$$\mathbf{a}_i^2(t+1) = \text{poslin} \left(\mathbf{a}_i^2(t) - \varepsilon \sum_{j \neq i} \mathbf{a}_j^2(t) \right). \quad (9)$$

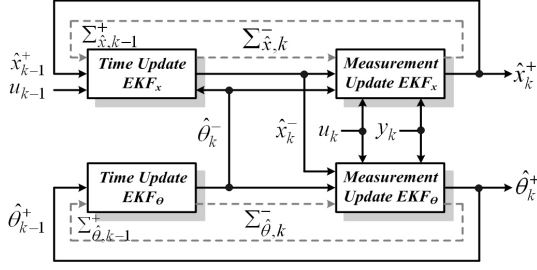


Fig. 3. Dual extended Kalman filter (DEKF).

Each neuron's output decreases in proportion to the sum of the other neuron's outputs. The output of the neuron with the largest initial condition decreases more slowly than the outputs of the other neurons. Eventually, only one neuron has a positive output. The index of the recurrent layer neuron with a stable positive output is the index of the prototype vector that best matched the input.

III. DUAL EXTENDED KALMAN FILTER (DEKF)

The DEKF is used to estimate the SOC/capacity of a Li-Ion cell. This algorithm combines the two EKFs, one of which is the state filter, which estimates the SOC, and the other is the weight filter, which estimates the capacity. At every time step, the state filter uses a priori value of the weight filter, while the weight filter uses a priori value of the state filter. Therefore, the two EKFs are calculated concurrently to estimate SOC/capacity. The DEKF can be reviewed by drawing a block diagram, as shown in Fig. 3.

The state-space representation with difference equations of the DEKF is described in (10)-(12).

$$x_{k+1} = f_x(x_k, u_k, \theta_k) + w_k^x \quad w_k^x \sim N(0, Q_k^x) \quad (10)$$

$$\theta_{k+1} = \theta_k + w_k^\theta \quad w_k^\theta \sim N(0, Q_k^\theta) \quad (11)$$

$$y_{k+1} = h_k(x_k, u_k, \theta_k) + v_k \quad v_k \sim N(0, R_k). \quad (12)$$

The symbols w_k^x and w_k^θ represent the process noise of the state filter and weight filter, respectively, are measured to be independent, zero-mean, Gaussian noise with covariance matrices Q_k^x and Q_k^θ . The measurement noise v_k is assumed to be independent, zero-mean, Gaussian noise with a covariance matrix R_k . Then the equations that decide the Kalman gain K_k^x and K_k^θ of the state filter and weight filter are as follows:

$$K_k^x = P_{\hat{x}_k}^- (H_k^x)^T [H_k^x P_{\hat{x}_k}^- (H_k^x)^T + R_k]^{-1} \quad (13)$$

$$K_k^\theta = P_{\hat{\theta}_k}^- (H_k^\theta)^T [H_k^\theta P_{\hat{\theta}_k}^- (H_k^\theta)^T + R_k]^{-1} \quad (14)$$

where the error covariance is P_k and the measurement sensitivity matrix is H_k .

The state-space equation of the cell model is derived as Eqs. (15)-(17), from the equivalent circuit shown in Fig. 1 and Δt is time between step k and $k+1$.

$$x_{k+1} = \begin{bmatrix} \text{SOC}_{k+1} \\ V_{\text{Diff},k+1} \end{bmatrix} = \begin{bmatrix} 1 & 0 \\ 0 & 1 - \frac{\Delta t}{R_{\text{Diff}} C_{\text{Diff}}} \end{bmatrix} \begin{bmatrix} \text{SOC}_k \\ V_{\text{Diff},k} \end{bmatrix} + \begin{bmatrix} -\frac{\Delta t}{C_{n,k}} \\ \frac{\Delta t}{C_{\text{Diff}}} \end{bmatrix} i_k + w_k^x \quad (15)$$

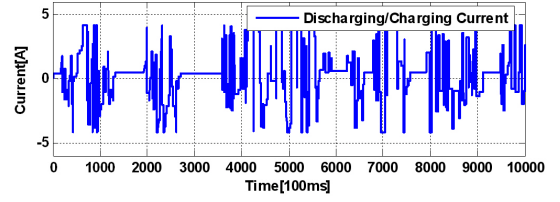


Fig. 4. Current profile for obtaining the DCVT pattern.

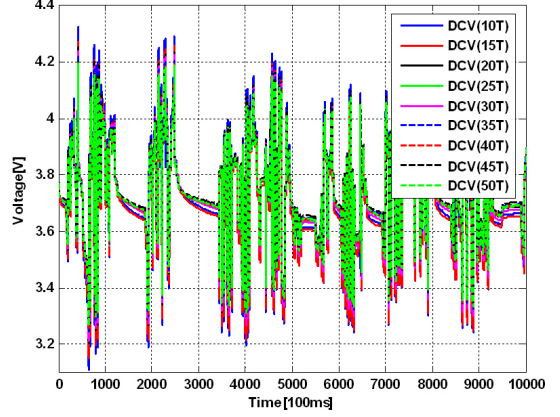


Fig. 5. Average DCVT patterns for ten fresh Li-Ion cells at various temperatures: 10–50°C with a 5°C temperature interval.

$$\theta_k = \begin{bmatrix} C_{n,k+1} \\ R_{i,k+1} \end{bmatrix} = \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix} \begin{bmatrix} C_{n,k} \\ R_{i,k} \end{bmatrix} + w_k^\theta \quad (16)$$

$$V_k = \text{OCV}(\text{SOC}_k, C_{n,k}) - V_{\text{Diff},k} - R_i i_k. \quad (17)$$

The OCV is the measurement equation is implemented by the relationship of the OCV-SOC data. The measurement matrix derived from Eqs. (18)-(21). From Eq. (20), the measurement matrix of the capacity C_n requires the total differential because the OCV is a function of the SOC. In this case, the first term of the right-hand side of Eq. (20) is irrelevant to the capacity. The measurement matrix of the series resistance R_i can be obtained from Eq. (21).

$$H_k^{\text{SOC}} = \frac{\partial V_i}{\partial \text{SOC}} = \frac{\partial \text{OCV}}{\partial \text{SOC}} \quad (18)$$

$$H_k^{V_{\text{Diff}}} = \frac{\partial V_i}{\partial V_{\text{Diff}}} = -1 \quad (19)$$

$$H_k^{C_n} = \frac{d\text{OCV}}{dC_n} = \left. \frac{\partial \text{OCV}}{\partial C_n} \right|_{C_n} + \frac{\partial \text{OCV}}{\partial \text{SOC}_k} \frac{d\text{SOC}_k^-}{dC_n} \quad (20)$$

$$H_k^{R_i} = \frac{\partial V_k}{\partial R_i} = -i_k \quad (21)$$

IV. PROPOSED APPROACH

A. Discharging/charging Voltage-temperature (DCVT) Pattern

After fully charging (SOC 100%) at a constant current of 4A, followed by a rest period, each cell was discharged to SOC 80%. Using a scaled-down discharging/charging current profile of a HEV, shown in Fig. 4, ten DCVT patterns were collected. The DCVT pattern is recorded for ten fresh Li-Ion cells at various temperatures: 10-50°C with a 5°C temperature interval. For pattern recognition, each average DCVT for ten fresh Li-Ion cells at various temperatures are obtained and shown in Fig. 5.

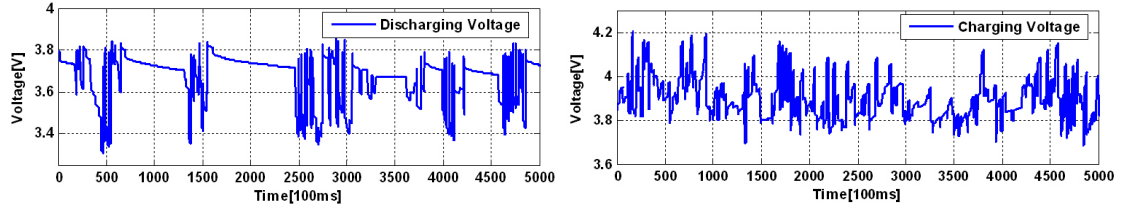


Fig. 6. Two patterns separated from the DCVT pattern. (a) Discharging-voltage temperature (DVT) pattern. (b) Charging-voltage temperature (CVT) pattern.

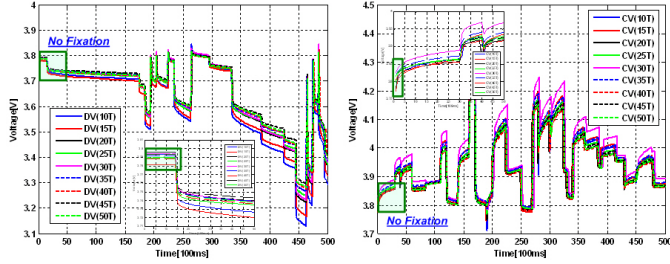


Fig. 7. Unfixed average discharging and charging voltages.

B. Initial Starting Voltage Points (ISVP) Fixation

For recognition of the DCVT pattern with the Hamming network, statistical analysis is necessary. First, all DCVT patterns are separated into two patterns according to current i : discharging voltage-temperature (DVT; $i \geq 0$) patterns and charging voltage-temperature (CVT; $i < 0$) patterns, as shown in Fig. 6. Second, the initial starting points of each DVT and CVT should be fixed. As shown in Fig. 7, the initial starting voltage points (ISVP) of the nine representative DCVT patterns are not fixed due to their various electrochemical characteristics at different temperatures. Hence, the averages and standard deviations of the collected discharging/charging voltages cannot be compared, and it is required to set a standard ISVP, as shown in Fig. 8. For example, consider three cells (A-C) with different ISVPs (VA1I-VC1I). It is required to choose one as the standard cell, B (VB1I=VB2I), and the other two voltages of A and C are up and down, respectively (VA1I=VA2I, VC1I=VC2I). Therefore, three ISVPs are fixed at one point. Based on this rule, the average and standard deviation for the voltage of A can be expressed in (22) and (23), respectively.

$$\text{Battery}_A^{\text{AVE}} = \frac{\sum_{i=1}^n V_{A2i}}{n} \quad (n = 1, 2, \dots, F) \quad (22)$$

$$\text{Battery}_A^{\text{STD}} = \sqrt{\frac{\sum_{i=1}^n (V_{A2i} - \text{Battery}_A^{\text{AVE}})^2}{n}} \quad (n = 1, 2, \dots, F) \quad (23)$$

In addition, for a given discharging condition, the average (AVE) and standard deviation (STD) of the three cells can be compared in (24) and (25), respectively.

$$\text{Battery}_A^{\text{AVE}} > \text{Battery}_B^{\text{AVE}} > \text{Battery}_C^{\text{AVE}} \quad (24)$$

$$\text{Battery}_A^{\text{STD}} < \text{Battery}_B^{\text{STD}} < \text{Battery}_C^{\text{STD}} \quad (25)$$

The fixed discharging/charging voltages are given in Fig. 9. All averages and standard deviations for the collected discharging/charging voltages can be compared through statistical analysis.

TABLE I
SEVEN CHARACTERISTICS PARAMETERS

Pattern	Parameter	Description
DCVT pattern	C1	Standard deviation DCVT
DVT pattern	C2	Standard deviation DVT
CVT pattern	C3	Standard deviation CVT
DVT pattern	C4	Average DVT (f)
	C5	Standard deviation DVT (f)
CVT pattern	C6	Average CVT (f)
	C7	Standard deviation CVT (f)

Standard : 25°C (discharging/charging) (f) : fixation

C. Characteristic Parameters of the DCVT Pattern

As indicated in Table I, characteristic parameters C1 to C7 are learned by the Hamming network using the average and standard deviations based on DCVT, DVT, and CVT patterns.

Each value of the seven characteristic parameters corresponding to the nine representative DCVT patterns is transformed into 1 and -1 element array with four levels, as shown in Fig. 10. If these patterns are not transformed into this binary form, then the pattern recognition performance can be distorted by a large real-valued parameter. In Fig. 10, avg is the average and std is the standard deviation of each characteristic parameter. The levels of each parameters are decided by three standard, viz., $avg - (\alpha \times std)$, m , and $avg + (\alpha \times std)$. The levels are decided according to the parameter values, as shown in Fig. 11. For example, if the value is larger than $avg - (\alpha \times std)$ and smaller than avg , the level is L3, and if the value is larger than avg and smaller than $avg + (\alpha \times std)$, the level is L2. α is a tuning value and is chosen as 0.5 to make the characteristic difference of nine representative patterns.

D. Pattern Recognition with the Hamming Network

As shown in Fig. 12, the feedforward layer calculates the inner product between each representative pattern and the current pattern. The seven characteristic parameters corresponding to nine representative DCVT patterns are transformed into the binary form and stored in the weight matrix \mathbf{W}^1 . The nine neurons storing the results of the inner product in the feedforward layer compete with each other to determine a winner. After this process, only one neuron will have a non-zero output, and this neuron indicates the representative pattern closest to the current pattern.

E. Temperature-checking Process

The representative DCVT pattern closest to an arbitrary DCVT pattern is selected. The outputs for the two layers of the Hamming network for three unknown DCVT patterns are shown in Figs. 13, 17, and 18. As shown in Fig. 13(b), the selected DCVT pattern closest to an arbitrary DCVT pattern

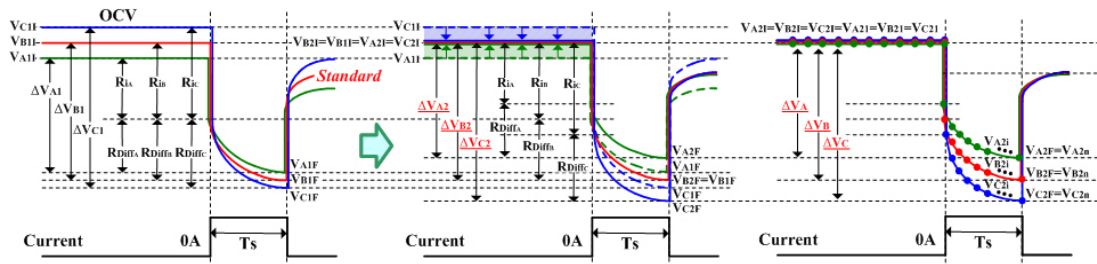


Fig. 8. Initial starting voltage points (ISVP) fixation.

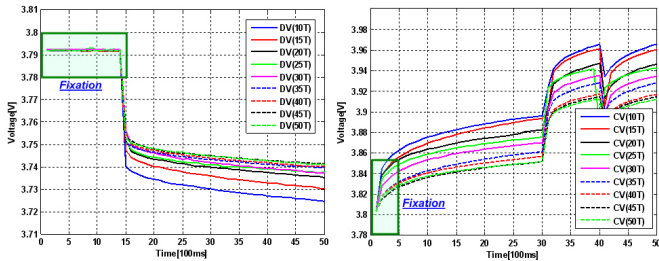


Fig. 9. Fixed average discharging and charging voltages.

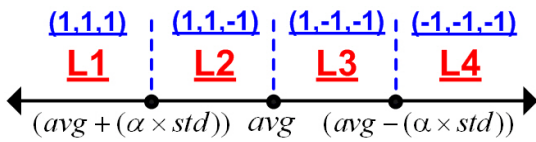


Fig. 10. Four levels as to three standards.

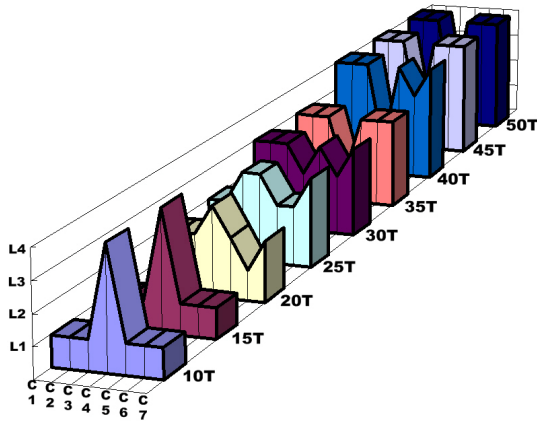


Fig. 11. Characteristics of nine representative patterns.

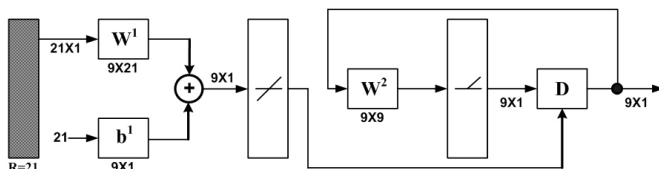
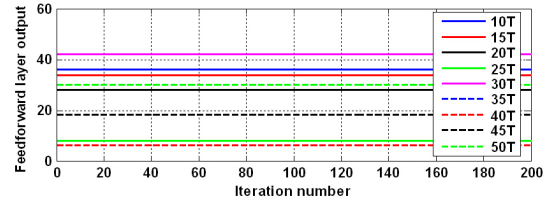
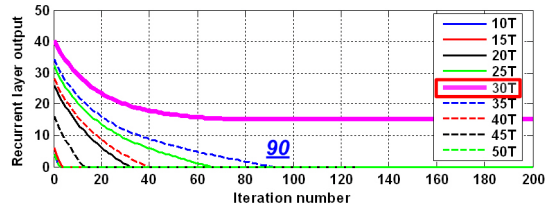


Fig. 12. Hamming network in this study.



(a)



(b)

Fig. 13. Outputs of two neural network layers (arbitrary 1, selected pattern 30°C). (a) Feedforward layer. (b) Recurrent layer.

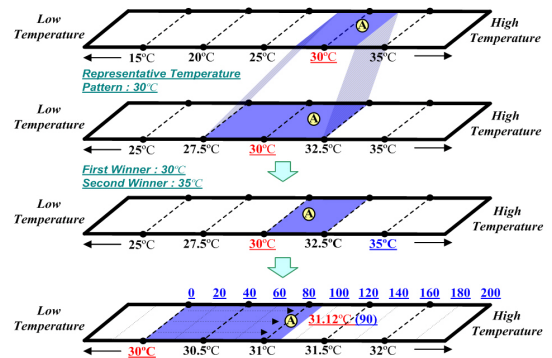
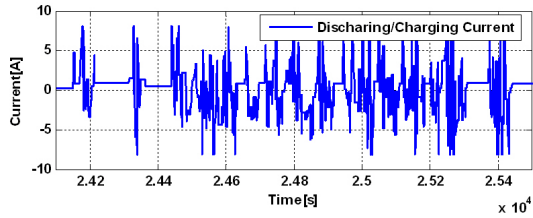
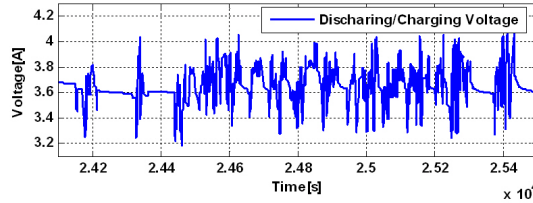


Fig. 14. Temperature-checking process.

1 is the DCVT pattern of 30°C as the closest match. The second- closest match is the DCVT pattern of 35°C. As aforementioned, one neuron that has a non-zero output is only selected as the first winner, or best fit, or the winner-take-all (WTA) operation. Then, two neurons with similar characteristics compete before the end of the selection of the first winner. Large differences in characteristics between two neurons lead to fast convergence to zero for the second winner. Conversely, small characteristic differences in characteristics between two neurons lead to slow convergence to zero. Based on this rule of the Hamming network, it is possible to check the approximate experimental temperature without a temperature sensor. The procedure of the temperature-checking process based on the Hamming network is shown in Fig. 14. When an

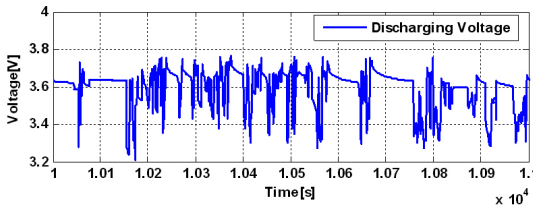


(a)

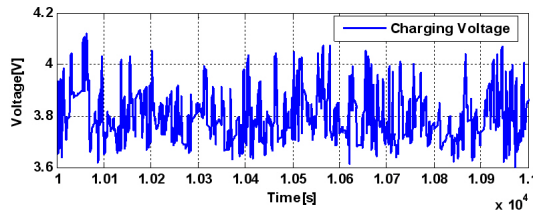


(b)

Fig. 15. Two plots for discharging/charging voltage-temperature (DCVT) pattern at an unknown temperature. (a) Current profile. (b) Voltage data.



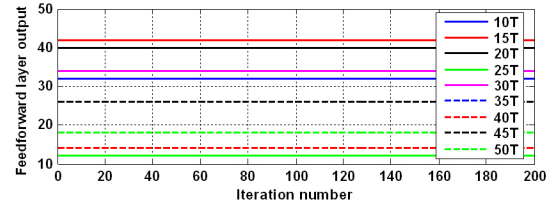
(a)



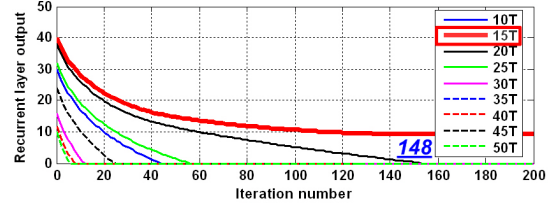
(b)

Fig. 16. Two separated voltages from the DCVT pattern at an unknown temperature. (a) Discharging voltage-temperature (DVT) pattern. (b) Charging voltage-temperature (CVT) pattern.

arbitrary DCVT patterns is applied as the input pattern, one DCVT pattern of 30°C is selected as the closest representative to the arbitrary DCVT pattern. Due to the 5°C temperature interval under experimental conditions, it is well known that the experimental temperature is in the range of 27.5°C to 32.5°C. In addition, as shown in Fig. 13(b), the other DCVT pattern of 35°C for the second winner is determined according to the competition result. Therefore, the experimental temperature can be expected in the range of 30.5°C to 32.5°C. For reference, if the other DCVT pattern of 25°C was the second winner, the expected range would be 27.5°C to 30.0°C. To accurately check the experimental temperature, the number of iterations when the recurrent layer output for the second winner is zero, after the competition, is required. As shown in Fig. 13(b), intercept of the recurrent layer output on the iteration-axis (0-200) for the second winner indicates the number of iterations. According to the expected temperature range of 2.5°C, the difference in elevated or reduced temperatures per one unit on the iteration-axis is 0.0125°C (2.5°C/200).

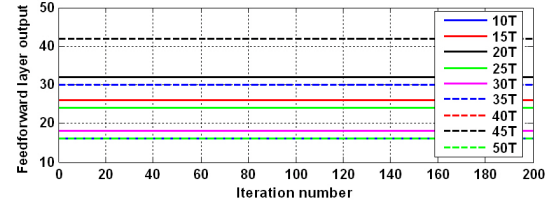


(a)

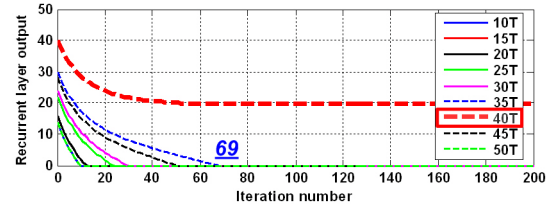


(b)

Fig. 17. Outputs of two neural network layers (arbitrary 2, selected pattern 15°C). (a) Feedforward layer. (b) Recurrent layer.



(a)



(b)

Fig. 18. Outputs of two neural network layers (arbitrary 3, selected pattern 40°C). (a) Feedforward layer. (b) Recurrent layer.

The starting-temperature point can be determined based on the selected DCVT pattern for the first winner. With the number of iterations, the difference in elevated or reduced temperatures can be obtained. In Fig. 13, the number of iterations to convergence to zero on the iteration-axis is 90. Given the first winner of the DCVT (30°C), the elevated temperature is 1.12°C (0.012590). Therefore, the estimated temperature result using the temperature-checking process for SOC/capacity estimation and SOH prediction using the DEKF is 31.12°C. This shows little difference when compared to the real temperature of 31°C. The measured empirical parameters measured in advance are modified and used as model parameters [15].

V. VERIFICATION

A. SOC/Capacity Estimation

In this section, SOC/capacity estimation results based on pattern recognition combined with empirical parameters are shown in detail. To verify the proposed approach, SOC/

TABLE II
NUMBERS OF ITERATION, TEMPERATURE ESTIMATED AND DIRECTLY MEASURED

Number of iteration	90	148	69
First/Second winners	30°C/25°C	15°C/20°C	40°C/35°C
Temperature estimated [°C]	31.12°C	16.85°C	39.14°C
Temperature measured [°C]	31°C	17°C	39°C

TABLE III
RESULTS OF ESTIMATED CAPACITY IN DEKF AND EXPERIMENTAL CAPACITIES DIRECTLY MEASURED

Cell	Arbitrary 1 (31.12°C)	Arbitrary 2 (16.85°C)	Arbitrary 3 (39.14°C)
Capacity estimated [Ah]	1.3184	1.1733	1.2874
Capacity measured [Ah]	1.3173	1.1751	1.2861

capacity estimation are performed at three experimental temperatures determined by the temperature-checking process.

Fig. 15(a) shows another scaled-down HEV automotive current profile was applied to the cell. The collected DCVT pattern shown in Fig. 15(b) was separated into the discharging and charging voltages, as shown in Fig. 16. Next, for the three unknown temperatures, based on the Hamming network, including the temperature-checking process, the suitable experimental temperature is determined. According to these temperatures, the pattern changes due to model parameter variation are implemented at an unknown temperature and compared with those at room temperature of 25°C, and used to implement a few simple techniques [15]. Finally, the obtained model parameters are used for SOC/capacity estimation. Three determined temperature results based on temperature-checking process are shown in Figs. 13(b), 17(b), and 18(b). For the results, the number of iterations for convergence to zero on the iteration-axis is 90, 148, and 69, respectively.

The number of iterations and determined temperature using the first winner pattern are shown in Table II. The estimated temperature is determined from the first winners of each recurrent layer and the number of iterations. The SOC and capacity estimate based on the DEKF for three unknown cells are compared with those of ampere-hour counting and measured capacity. Measurements are shown in Fig. 19, and results are summarized in Table III. At any temperature, the SOC and capacity estimation results of the DEKF satisfy the specification within $\pm 5\%$.

B. SOH Prediction

The direct current internal resistance (DCIR) [16] is defined as the magnitude of the lumped parameter, namely $R_i + R_{Diff}$, is used to obtain the SOH based on the pulse power analysis [7], [8]. Therefore, in order to overcome erroneous SOH prediction, it is important to know the DCIR variance at various temperatures. The DCIR experiments were carried out for DCIR measurement at various temperatures using 10 fresh Li-Ion cells previously used to obtain the DCVT pattern. The average constant DCIRs of 10 fresh Li-Ion cells at SOC 60% are shown in Fig. 20. The DCIR increased with a decrease of temperature. Specifically, the marked difference in DCIR among the cells is shown below room temperature and more increased at reduced temperatures of 10-20°C. The pattern changes of the DCIR at a specified temperature when compared with that at room temperature are used to implement

TABLE IV
 $Maxd_Coef$, $Mind_Coef$, AND $Temp_Coef$ VALUES AT VARIOUS TEMPERATURES

Temperatures	10°C	15°C	20°C	25°C	30°C
$Maxd_Coef$	0.9086	0.9096	0.9105	0.9357	0.9520
$Mind_Coef$	1.0713	1.0654	1.0595	1.0554	1.0307
$Temp_Coef$	0.10	0.15	0.15	0.20	0.15

Temperatures	35°C	40°C	45°C	50°C
$Maxd_Coef$	0.9662	0.9805	0.9813	0.9822
$Mind_Coef$	1.0291	1.0275	1.0241	1.0208
$Temp_Coef$	0.10	0.05	0.05	0.05

TABLE V
DCIRs OF FULLY FRESH AND AGED CELLS AT VARIOUS TEMPERATURES

Temperature	Fully fresh cell DCIR [Ω]	Fully aged cell DCIR [Ω]
10°C	0.12896	0.19724
20°C	0.07921	0.12617
25°C	0.05896	0.09875
30°C	0.05586	0.08719
40°C	0.04229	0.06824
50°C	0.03832	0.06221

the DCIR-temperature relation for SOH prediction. First of all, given average, maximum, and minimum DCIRs among 10 Li-Ion cells at various temperatures, $Maxd_Coef$ and $Mind_Coef$ can be expressed in Eqs. (26) and (27), respectively.

$$Maxd_Coef = \frac{\text{Average DCIR}}{\text{Maximum DCIR}} < 1 \quad (26)$$

$$Mind_Coef = \frac{\text{Average DCIR}}{\text{Minimum DCIR}} > 1 \quad (27)$$

Nine coefficients corresponding to experimental temperature range are each determined at various experimental temperatures (10-50°C with a 5°C temperature interval), shown in Fig. 21 and Table IV. Considering a weighted coefficient $Temp_Coef$ at experimental temperature range of 10-50°C, the $Maxd(mod)$ and $Mind(mod)$ can be computed from the sum of the multiplication between $Maxd_Coef$ ($Mind_Coef$) and $Temp_Coef$ in Eqs. (28) and (29), respectively.

$$Maxd(mod) = Maxd_Coef \times Temp_Coef = 1.0481 \quad (28)$$

(10 ~ 50°C; 5°C interval)

$$Mind(mod) = Mind_Coef \times Temp_Coef = 0.9376 \quad (29)$$

(10 ~ 50°C; 5°C interval)

As expressed in (30), the SOH of an arbitrary cell can be predicted using the selected cell pattern's DCIR, $DCIR_{selected}$.

$$SOH_{arbitrary} = \left| \frac{DCIR_{selected} - DCIR_{aged}}{DCIR_{fresh} - DCIR_{aged}} \right| \quad (30)$$

where $DCIR_{fresh}$ and $DCIR_{aged}$ are each DCIRs of fully fresh and aged cells among the 1.3Ah group. Under the identical experimental condition of Fig. 20, each DCIR of fully fresh and aged cells are listed in Table V.

Using the determined temperature (31.12°C) based on the temperature-checking process, the SOH of an arbitrary cell can be obtained. The procedure is shown in Table VI.

VI. CONCLUSIONS

Precise SOC/capacity estimation and SOH prediction are critical for practical applications and to prevent over-charging

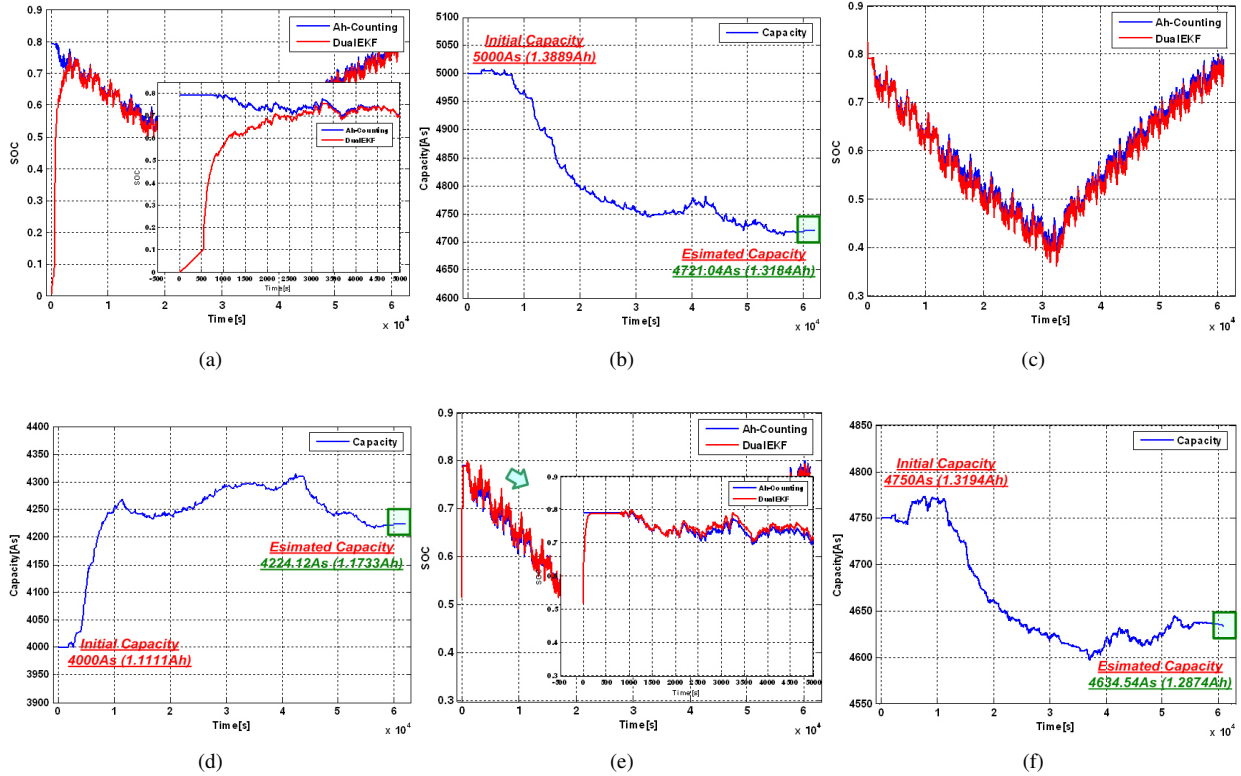


Fig. 19. SOC/capacity estimation based on DEKF in comparison with ampere-hour counting and measured capacity. (a)(b) SOC (Initial SOC 0), Capacity (Initial capacity 1.3889Ah);Unknown cell 1, Arbitrary temperature 31°C, selected pattern 30°C. (c)(d) SOC (Initial SOC 0.793), Capacity (Initial capacity 1.1111Ah);Unknown cell 2, Arbitrary temperature 17°C, selected pattern 15°C. (e)(f) SOC (Initial SOC 0.5), Capacity (Initial capacity 1.3194Ah);Unknown cell 3, Arbitrary temperature 39°C, selected pattern 40°C.

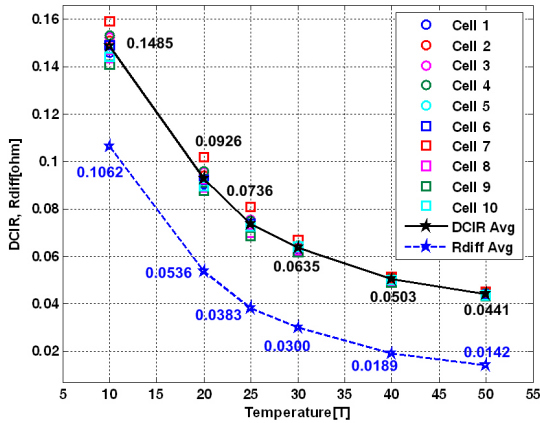


Fig. 20. Average values of the DCIR at SOC 60% (temperatures: 10-50°C with a 10°C temperature interval).

TABLE VI
PROCEDURE FOR OBTAINING THE SOH OF AN UNKNOWN CELL 1 AFTER THE TEMPERATURE-CHECKING PROCESS (31.12°C, SOC 27%)

Direct current internal resistance (DCIR)	
DCIR _{fresh} /DCIR _{aged} at 30°C, 40°C: 0.0559Ω, 0.0423Ω / 0.0872Ω, 0.0682Ω (X-axis : temperature, Y-axis : DCIR)	
$y = \frac{(0.0423 - 0.0559)\Omega}{(40 - 30)^\circ\text{C}}(x - 40^\circ\text{C}) + 0.0423\Omega$	
DCIR_{fresh} at 31.12°C: 0.0544Ω	
$y = \frac{(0.0682 - 0.0872)\Omega}{(40 - 30)^\circ\text{C}}(x - 40^\circ\text{C}) + 0.0682\Omega$	
DCIR_{aged} at 31.12°C: 0.0851Ω	
Average DCIR at 30°C, 40°C: 0.0635Ω, 0.0503Ω (Arbitrary cell)	
$y = \frac{(0.0503 - 0.0635)\Omega}{(40 - 30)^\circ\text{C}}(x - 40^\circ\text{C}) + 0.0503\Omega$	
Average DCIR at 31.12°C: 0.0621Ω	
Maxd(mod)= 0.9376, Mind(mod)= 1.0481	
0.0621/Maxd(mod)=0.0662, 0.0621/Mind(mod)=0.0592	
DCIR range of 31.12°C : 0.0592(DCIR _{min}) ~ 0.0662Ω(DCIR _{max})	
$SOH_{arbitrary_max} = \frac{DCIR_{selected} - DCIR_{aged}}{DCIR_{fresh} - DCIR_{aged}} = \frac{0.0592 - 0.0851}{0.0544 - 0.0851} \approx 0.8436$	
$SOH_{arbitrary_min} = \frac{DCIR_{selected} - DCIR_{aged}}{DCIR_{fresh} - DCIR_{aged}} = \frac{0.0662 - 0.0851}{0.0544 - 0.0851} \approx 0.6156$	
Predicted SOH range of an arbitrary cell at 31.12°C : 0.6156~0.8436	

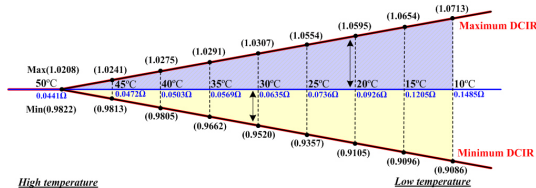


Fig. 21. DCIR-temperature relation for SOH prediction.

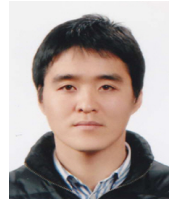
and over-discharging, which may cause permanent internal damage. This study presents an application of the Hamming network-DEKF based on pattern recognition for improved SOC/capacity estimation and SOH prediction at various temperatures. This method avoids the need for repeated measurement of cell parameters before SOC/capacity estimation and SOH prediction process.

ACKNOWLEDGMENT

This work was supported by the New and Renewable Energy Program of the Korea Institute of Energy Technology Evaluation and Planning (KETEP) grant funded by the Korea government Ministry of Knowledge Economy (NO. 20104010100490).

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