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# A New Synchronous Reference Frame-Based Method for Single-Phase Shunt Active Power Filters

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#### **Abstract**

This paper discusses the design of a novel synchronous reference frame (SRF) method that can extract the reference compensating current for single-phase shunt active power filters (APFs). Unlike previous SRF studies, the proposed method has an innovative feature that does not require a fictitious current signal. Other key features of the proposed strategy include frequency-independent operation, accurate reference current extraction, and relatively fast transient response. The effectiveness of the proposed method is investigated by conducting a detailed mathematical analysis. Results of the analysis confirm the superior performance of the suggested approach. Theoretical evaluations are confirmed by the experimental results.

Keywords: Active filter, Synchronous reference frame, Power quality

### I. INTRODUCTION

With the present increasing use of non-linear loads, particularly power electronic equipments, and with emerging power quality issues, both energy consumers and the electrical supply industry demand innovative and cost-effective power quality enhancement solutions. Traditionally, shunt-connected LC passive filters, tuned at dominant harmonic frequencies had been used to absorb harmonic currents generated by non-linear loads. Besides their bulky sizes and heavy weights, these filters suffer from serious technical drawbacks such as (1) susceptibility to resonances with the load and line impedances. (2) sensitivity to component tolerances and system parameter variations, and (3) possibility of overloading by ambient harmonic loads. Active power filters (APFs) have been proven successful in overcoming the above problems. An APF is a power electronic converter-based device designed to compensate current and voltage disturbances caused by non-linear loads. Compared with passive filters, APFs offer superior filtering performance, smaller size, and flexibility in the application. Although the cost of APFs is high, severe harmonic distortion limits in passive filters have drastically

Manuscript received Sep. 7, 2012; revised Apr. 5, 2013 Recommended for publication by Associate Editor Kyo-Beum Lee. increased their installation and operating costs, thus resulting in APFs to be considered as economically viable. Many manufacturers have already placed APFs on the market and cause a strong competition in terms of cost and performance. APFs, consequently, are becoming affordable and can challenge traditional passive filters in high-demand applications [1]. APFs can be classified as either three- or single-phase APFs. The current study focuses on the latter type. Single-phase APFs can be used in low- and medium-power applications, particularly in single-phase adjustable speed motor drives, arc furnaces [2], air-conditioning systems [3], electric tractions [4], electronic fluorescent lamp ballasts [5], and so on. In terms of their function, APFs can also be classified into four major categories, namely, (1) shunt APFs, (2) series APFs, (3) hybrid APFs, and (4) unified power quality conditioners (UPQCs) [6]. Generally, a single-phase shunt APF, which is the subject of the present study, can operate in either perfect harmonic cancellation (PHC) or in unity power factor (UPF) mode [7]. In the PHC mode, the shunt APF compensates the total harmonic contents required by the non-linear load, while, in the UPF mode, the reactive current component drawn by the non-linear load is compensated in addition to harmonic currents. Fig. 1 shows a schematic diagram of a single-phase shunt APF. The control system consists of the following main parts: (1) the reference current generation (RCG) block responsible for the extraction of the total harmonic content and of the reactive component of the distorted current drawn by the

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non-linear load when necessary, (2) the DC-link voltage control block that regulates the active power balance between APF and the grid, and (3) the current control block that generates appropriate gating signals for the voltage source inverter (VSI), allowing the output current of VSI to track the reference command delivered by the RCG block.

Different techniques can be applied to generate the reference-compensating current for single-phase shunt APFs [5]-[13]. Among them, the synchronous reference frame (SRF) or the dq theory, which is widely applied in three-phase systems, is considered as one of the simplest and most preferred techniques. The SRF theory can achieve fast and accurate extraction of the harmonic content and of the reactive component of a distorted current, but the application has only been focused in three-phase APFs. Saitou et al. extended the application of the SRF theory to single-phase APFs [14]. In their method, as shown in Fig. 2, the fundamental component of the single-phase signal is shifted by 90° to generate a fictitious phase signal. Thus, it is possible to represent the single-phase APF as a pseudo two-phase ( $\alpha\beta$ ) system. Detailed information on the construction of the fictitious phase can be found in [15] and [16]. Although this technique achieves a desired harmonic compensation, its implementation has some drawbacks that include the following: (1) the use of the Hilbert transform to construct the fictitious phase requires high computational load, (2) given the sensitivity of the algorithm to grid frequency fluctuations, any variation in the grid frequency introduces an error in the calculated reference current, and (3) reactive power compensation is not considered in this method. To overcome some of the aforementioned drawbacks, Kim et al, proposed a control technique that is referred in this paper as the double synchronous reference frame (DSRF) method [17]. In the DSRF method, the fictitious phase is constructed by using a low-pass filter and two SRFs (dq<sup>+</sup> and dq<sup>-</sup>) rotating in opposite directions to generate the reference current. The succeeding section demonstrates that, from a control point of view, the DSRF method is equivalent to the control technique depicted in Fig. 2, except that the fictitious phase (β-axis signal) is set to zero. Poor performance in reactive power compensation, unnecessary complexity, high computational burden for the control system are main drawbacks of the DSRF method.

As previously discussed, the fictitious current signal is a common feature in all previously reported RCG techniques in SRF. This paper therefore proposes a novel RCG technique in SRF, which does not require a fictitious current signal and exhibits frequency-independent operation, accurate reference current extraction, and relatively fast transient response.

A detailed analysis of the proposed strategy under different operating conditions of a shunt APF is presented in this study. The results indicate the superior performance of the proposed method. The theoretical evaluations are confirmed through experiments.

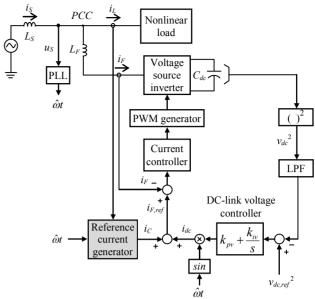


Fig. 1. Schematic diagram of a single-phase shunt APF.

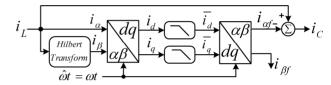


Fig. 2. Basic scheme of the SRF harmonic extraction technique [14].

### II. REVIEW OF DSRF METHOD AND INTRODUCTION TO PROPOSED METHOD

Fig. 3 shows the basic scheme of the DSRF method [17].  $i_{\alpha\beta}$  and  $i_C$  are the extracted fundamental and harmonic components of the non-linear load current, respectively.

Moving the sums ahead of LPFs simplifies the DSRF method, as shown in Fig. 4, from which the following equations are obtained:

$$i_d = i_{d,pos} - i_{d,neg} \tag{1}$$

$$i_q = i_{q,pos} + i_{q,neg} \tag{2}$$

The  $\alpha\beta$  reference frame is transformed into dq<sup>+</sup> and dq reference frames, as expressed in the following matrix equations [17]:

$$\begin{bmatrix} i_{d,pos} \\ i_{q,pos} \end{bmatrix} = T(t) \begin{bmatrix} i_{\alpha} \\ i_{\beta} \end{bmatrix}$$
 (3)

$$\begin{bmatrix} i_{d,neg} \\ i_{q,neg} \end{bmatrix} = T \left( -t \right) \begin{bmatrix} i_{\alpha} \\ i_{\beta} \end{bmatrix} \tag{4}$$

where

$$T(t) = \begin{bmatrix} \sin(\omega t) & -\cos(\omega t) \\ -\cos(\omega t) & -\sin(\omega t) \end{bmatrix}$$
 (5)

By substituting (3) and (4) into (1) and (2),  $i_d$  and  $i_q$  are written as follows:

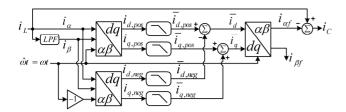


Fig. 3. Basic scheme of the DSRF method [17].

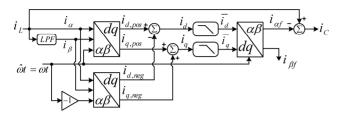


Fig. 4. Simplification of the DSRF method using block diagram algebra.

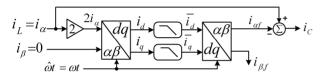


Fig. 5. Simplified equivalent of the DSRF method.

$$i_d = 2\sin(\omega t)i_{\alpha} \tag{6}$$

$$i_{\alpha} = -2\cos(\omega t)i_{\alpha} \tag{7}$$

In matrix form, (6) and (7) are rewritten as

$$\begin{bmatrix} i_d \\ i_q \end{bmatrix} = \begin{bmatrix} \sin(\omega t) & -\cos(\omega t) \\ -\cos(\omega t) & -\sin(\omega t) \end{bmatrix} \begin{bmatrix} 2i_\alpha \\ 0 \end{bmatrix} = T(t) \begin{bmatrix} 2i_\alpha \\ 0 \end{bmatrix}$$
(8)

Using the above matrix equation, the DSRF method illustrated in Fig. 4 can be further simplified to that which is exhibited in Fig. 5.

From the control point of view, the control structure shown in Fig. 5 is the exact equivalent of the DSRF technique presented in Fig. 3. Therefore, the DSRF method imposes unnecessary complexity and high computational burden to the control system.

The drawbacks of the DSRF method are further investigated by performing a simple mathematical analysis described in the succeeding paragraphs.

The non-linear load current is represented by [11]

$$i_L = I_m \sin(\omega t - \phi) + \sum_{k=3,5,7} I_k \sin(k \omega t - \phi_k)$$
 (9)

where  $\omega$  is the grid voltage fundamental frequency,  $I_m$  and  $\phi$  are the amplitude and the phase of the fundamental current, respectively, and  $I_k$  and  $\phi_k$  are the magnitude and the phase of the  $k^{th}$  harmonic-current component, respectively.

By substituting (9) into (6) and (7),  $i_d$  and  $i_q$  are obtained as

$$i_d = I_m \cos(\phi) - I_m \cos(2\omega t - \phi)$$

$$+ \sum_{k=3,5,7,...} I_k \left[ \cos((k-1)\omega t - \phi_k) - \cos((k+1)\omega t - \phi_k) \right]$$
(10)

$$i_{q} = I_{m} \sin(\phi) - I_{m} \sin(2\omega t - \phi)$$

$$- \sum_{k=3,5,7,...} I_{k} \left[ \sin((k-1)\omega t - \phi_{k}) - \sin((k+1)\omega t - \phi_{k}) \right]$$
(11)

where the constant terms  $I_m cos(\phi)$  and  $I_m sin(\phi)$  correspond to the magnitude of the active and of the reactive currents, respectively, of the non-linear load.

From (10) and (11),  $i_d$  and  $i_q$  obviously contain significant second-order harmonic content. Thus, the removal of these variables requires the use of a first- or a second-order LPF with low cut-off frequency or of a high-order LPF. A high-order LPF imposes high computational load to the control system, in addition to stability problems [18]. By contrast, using a low-order LPF with low cut-off frequency significantly degrades the transient performance of APF.

With the foregoing analysis, the efficiency of the control technique reported by Kim et al. [17] is significantly undermined.

To cancel out undesired double-frequency oscillations without degrading the stability and the transient performance of the system, a novel method called the double-frequency oscillation cancellation (DFOC) method is presented in this paper. It is worth mentioning that the cancellation of double-frequency oscillations is already attended in unbalanced three-phase systems [19]. The performance of this approach is investigated by conducting a detailed mathematical analysis.

#### III. DFOC METHOD

This section mainly focuses on eliminating double-frequency ripples  $I_m cos(2\omega t - \phi)$  and  $I_m sin(2\omega t - \phi)$  from  $i_d$  and  $i_q$ , respectively. Thus, for the sake of simplicity,  $i_d$  and  $i_q$  are considered as

$$i_d = I_m \cos(\phi) - I_m \cos(2\omega t - \phi) \tag{12}$$

$$i_q = I_m \sin(\phi) - I_m \sin(2\omega t - \phi) \tag{13}$$

Expanding (12) and (13) gives

$$i_d = I_m \cos(\phi) - I_m \cos(\phi) \cos(2\omega t) - I_m \sin(\phi) \sin(2\omega t)$$
 (14)

$$i_a = I_m \sin(\phi) + I_m \sin(\phi) \cos(2\omega t) - I_m \cos(\phi) \sin(2\omega t)$$
 (15)

The amplitudes of the double-frequency oscillation terms obviously depend on the average values of  $i_d$  and  $i_q$ . The perfect cancellation of these undesired components can be easily achieved by injecting double-frequency signals with the same amplitude, except the opposite phase angle, into  $i_d$  and  $i_q$ . Fig. 6 illustrates the schematic diagram of the proposed strategy, where  $\hat{\omega}$  denotes the estimated frequency by PLL.

In this section, we assume that  $\hat{\omega} = \omega$ . The LPF block is subsequently considered as a first-order LPF, expressed as follows:

$$LPF(s) = \frac{\omega_c}{s + \omega_c} \tag{16}$$

where  $\omega_c$  is the cut-off frequency of LPF.

To analyze the performance of the proposed method, the mathematical expressions for  $\overline{i_d}$  and for  $\overline{i_q}$  are derived.

$$\overline{t_d} = A_d + \{B_d \cos(\omega t)\cos(\omega_f t) + C_d \sin(\omega t)\sin(\omega_f t) + D_d \sin(\omega t)\cos(\omega_f t) + D_d \sin(\omega t)\cos(\omega_f t) + E_d \cos(\omega t)\sin(\omega_f t)\}e^{-\omega_C t}$$
(17)

$$\overline{t_q} = A_q + \left\{ B_q \cos(\omega t) \cos(\omega_f t) + C_q \sin(\omega t) \sin(\omega_f t) + D_q \sin(\omega t) \cos(\omega_f t) + E_q \cos(\omega t) \sin(\omega_f t) \right\} e^{-\omega_C t}$$
(18)

where

$$\begin{split} & \omega_f = \sqrt{\omega^2 - \omega_c^2} \ , \\ & A_d = -B_d = -D_q = I_m \cos(\phi) \ , \\ & C_d = -E_q = -I_m \left[\omega\cos(\phi) + \omega_c \sin(\phi)\right]/\omega_f \ , \\ & D_d = A_q = -B_q = I_m \sin(\phi) \ , \text{ and} \\ & E_d = C_q = -I_m \left[\omega_c \cos(\phi) + \omega\sin(\phi)\right]/\omega_f \ . \end{split}$$

Appendix A presents the detailed derivation of (17) and (18). To highlight the advantages of the proposed method, the mathematical expressions for  $\overline{i_d}$  and for  $\overline{i_q}$  without using the DFOC block are also presented. These expressions are as follows:

$$\overline{i}_{d} = I_{m}cos(\phi) - (A'_{d} + I_{m}cos\phi)e^{-act} 
+ A'_{d}cos(2\omega t) + B'_{d}sin(2\omega t)$$
(19)

$$\overline{i_q} = I_m \sin(\phi) - (A_q' + I_m \sin\phi)e^{-a_C t} 
+ A_q' \cos(2\omega t) + B_q' \sin(2\omega t)$$
(20)

where

$$A'_d = B'_q = I_m \omega_c (2\omega \sin \varphi - \omega_c \cos \phi) / (4\omega^2 + \omega_c^2), \text{ and}$$

$$B'_d = -A'_q = -I_m \omega_c (2\omega \cos \phi + \omega_c \sin \varphi) / (4\omega^2 + \omega_c^2).$$

From (17) and (18), the fluctuating terms obviously decay to zero with a time constant of  $1/\omega_c$  and  $\overline{i_d}$  and  $\overline{i_q}$  converge to  $I_m \cos(\phi)$  and  $I_m \sin(\phi)$ , respectively. However, in (19) and (20),  $\overline{i_d}$  and  $\overline{i_q}$  contain high-amplitude, double-frequency contents. These results are clearly illustrated in Fig. 7 for  $I_m = 10$  A,  $\phi = \pi/3$  rad,  $\omega_c = 50$  rad/s, and  $\omega = 100\pi$  rad/s. As expected, perfect cancellation of double-frequency is achieved when the DFOC block is used.

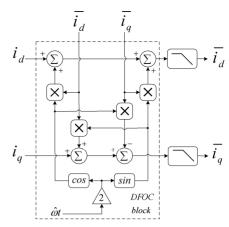


Fig. 6. Schematic diagram of the DFOC method

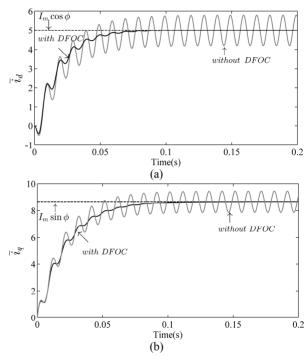


Fig. 7. Comparative results for  $i_d$  and  $i_q$ . (a)  $i_d$  with (black line) and without (gray line) the use of the DFOC block. (b)  $i_q$  with (black line) and without (gray line) the use of the DFOC block

# IV. APPLICATION OF THE DFOC METHOD IN DIFFERENT CONTROL MODES OF A SHUNT APF

This section investigates the application of the DFOC block in extracting the reference-compensating current in different control strategies of a shunt APF (i.e., UPF and PHC modes of operation).

#### A. PHC Mode

Fig. 8 shows the application of the DFOC block in extracting the reference-compensating current in the PHC mode of operation. The figure illustrates that the suggested strategy does not require a fictitious current signal.

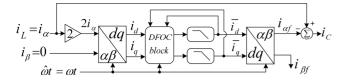


Fig. 8. Proposed method to extract the reference-compensating current in the PHC mode of a shunt APF.

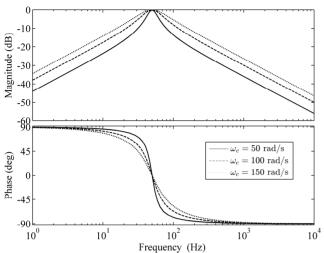


Fig. 9. Bode diagram of transfer function (21) for three different values of cut-off frequency  $\omega_c$ .

To evaluate the performance of the proposed method, the transfer function of the extracted fundamental current to the load current is expressed as follows:

$$G_{DFOC} = \frac{i_{\alpha f}}{i_L} = \frac{2\omega_c s}{s^2 + 2\omega_c s + \omega^2}$$
 (21)

A detailed derivation of (21) can be found in Appendix B.

Fig. 9 shows the bode plot of transfer function (21) for three different values of cut-off frequency  $\omega_c$ . We observe that transfer function (21) exhibits a band-pass filtering behavior with a pass-band centered at the fundamental frequency. The bandwidth of the band-pass filter (BPF) is proportional to the cut-off frequency of LPF. The lower the cut-off frequency, the less is the BPF bandwidth and the better is the filtering performance.

The transient behavior of the proposed algorithm can be analyzed by identifying the closed loop poles of transfer function (21), which are written as follows:

$$p_1 = -\omega_c + j\,\omega, \ p_2 = -\omega_c - j\,\omega \tag{22}$$

As shown in (22), the transient time is dictated by the cut-off frequency of LPF. The higher the cut-off frequency, the faster is the transient response, as stated in [20] and [21].

The dc-offset rejection capability is another important feature that must be examined. As expressed in (21), transfer function  $G_{DFOC}$  has a zero located at the origin. Thus, the perfect rejection of the dc-offset is achieved for all values of  $\omega_c$ .

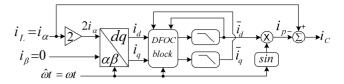


Fig. 10. Proposed method to extract the reference-compensating current in the UPF mode of a shunt APF.

Finally, the performance of the proposed algorithm is investigated under grid frequency deviations. Notice that the transfer function (21) is derived from the assumption that  $\hat{\omega} = \omega$ . Without this assumption, the transfer function of the extracted fundamental current to the load current can easily be demonstrated as

$$G_{DFOC} = \frac{2\omega_c s}{s^2 + 2\omega_c s + \hat{\omega}^2} \tag{23}$$

Substituting the Laplace operator  $s = j\omega$  into (23) yields

$$G_{DFOC}|_{s=j\omega} = \frac{j \, 2\omega_c \omega}{\hat{\omega}^2 - \omega^2 + j \, 2\omega_c \omega}$$
 (24)

From (24), under variations in the grid frequency, if the estimated frequency by PLL is very close (or preferably equal) to the actual frequency, the fundamental-current component is perfectly extracted, ultimately achieving an accurate reference-compensating current.

### B. UPF Mode

Fig. 10 shows the application of the DFOC block in extracting the reference-compensating current in the UPF mode of operation of a single-phase shunt APF, where  $i_n$  denotes the extracted active current drawn by the load. By performing a procedure similar with that discussed in section IV-A, we can obtain the same operational characteristics for the system presented in Fig. 10, with the following: frequency-independent operation and 2) dependence of the transient response and of the filtering accuracy to the cut-off frequency of LPF.

## V. OUTPUT CURRENT AND DC-LINK VOLTAGE CONTROLLERS

The control scheme presented in Fig. 1 clearly indicates that the performance of the APF system highly depends on the quality of the current controller. If the converter model in Fig. 1 is simplified to  $1/(L_F s + R_F)$ , where  $L_F$  and  $R_F$  are filter inductance and resistance, respectively, and assuming that the current controller is composed of  $k_{pc} + k_{ic}/s$  (Fig. 11(a)), then the closed-loop transfer function of the current controller is easily derived as

$$\frac{i_F}{i_{F,ref}} = \frac{k_{pc}s + k_{ic}}{Ls^2 + (R + k_{pc})s + k_{ic}}$$
(25)

where  $i_F$  and  $i_{F,ref}$  are the measured and the reference currents for APF, respectively.

The choice of controller gains is obviously a compromise between the stability margin and the dynamic performance. In this study, the selected bandwidth is 2 kHz that effectively covers the 39<sup>th</sup> harmonics and is significantly lower than the switching frequency, thus preventing sensitivity to switching noises.

On the other hand, as depicted in Fig. 1, a controller must regulate the DC-link voltage to maintain the energy balance between APF and the grid. In practice, converter losses and leakage of DC-link capacitors cause the active filter to draw a fundamental active component that maintains the DC-link voltage at its pre-defined value [22]-[25]. The DC-link voltage controller is adopted from [3] in which the small-signal model of the DC-link voltage loop is derived as presented in Fig. 11(b), where  $C_{dc}$  is the total DC-link capacitance,  $U_{ps}$  is the peak value of the PCC voltage,  $\omega_{cv}$  = 60 rad/s is the cut-off frequency of the low-pass filter, and  $k_{pv}$  and  $k_{iv}$  are the gains of the PI regulator.

A significantly lower bandwidth for the voltage loop than that of the current loop is selected to avoid interaction between the DC-link voltage and the output current controllers. Indeed, a bandwidth that is not larger than one tenth of the grid frequency and a phase margin that ranges from 30° to 60° are recommended [3].

#### VI. EXPERIMENTAL RESULTS

To confirm the performance of the proposed algorithm, the system shown in Fig. 1 is developed in the laboratory. A TMS320F28335 digital signal controller (DSC) from the Texas Instruments is used to perform the algorithm and to generate the proper gating signals for the single-phase converter. Table I shows the salient parameters of the system. For all experiments, the selected cut-off frequency of LPF is 95 rad/s.

As a worst case scenario, the performance of the proposed APF system is investigated in the presence of a highly non-linear load consisting of a diode rectifier bridge feeding a capacitor in parallel with a resistor. As shown in Fig. 12, the load current is highly distorted with zero periods, while the source current remains sinusoidal and in-phase with the grid voltage. The current quality can be further improved by using the DFOC block. In this case, the total harmonic distortion (THD) is reduced to 4.18% and the lowest order harmonic (3<sup>rd</sup> harmonic) is drastically attenuated.

In another study, as depicted in Fig. 13, a resistive load is being operated and the system is at a steady state. Suddenly, a non-linear rectifier load is switched on. The transient period is approximately two cycles of the fundamental frequency, which is relatively longer than that required for the FFT method by [26], almost equal to that in [27], and shorter compared with that in [28]. The source current and the grid voltage are initially in-phase and after the transient period, the phase difference is again converged to zero in the steady state, which means the unity power factor is achieved.

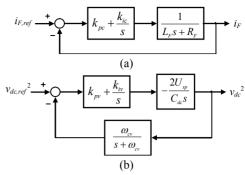


Fig. 11. Small-signal model of (a) output current and of (b) DC-link voltage loops [3].

TABLE I System Parameters

| Quantity                   | Description                   | Value        |
|----------------------------|-------------------------------|--------------|
| $u_{\scriptscriptstyle S}$ | AC power source voltage (rms) | 110 V        |
| f                          | AC power source frequency     | 50 Hz        |
| $L_s$                      | Grid inductance               | 80 μΗ        |
| $C_{dc}$                   | DC capacitance                | 4800 μF      |
| $f_s$                      | Switching frequency           | 20 kHz       |
| $f_{sam}$                  | Sampling frequency            | 40 kHz       |
| $L_F$                      | Filter inductance             | 2.5 mH       |
| $R_F$                      | Filter resistance             | $0.5 \Omega$ |

TABLE II
THD AND TRANSIENT TIME OF THE SOURCE CURRENT FOR
DIFFERENT CUT-OFF FREQUENCY VALUES OF LPF

| $\omega_c (rad / s)$ | THD (%) | Transient time (ms) |
|----------------------|---------|---------------------|
| 50                   | 1.91    | 45                  |
| 75                   | 2.44    | 30                  |
| 100                  | 2.50    | 24                  |
| 125                  | 2.52    | 15                  |
| 150                  | 2.75    | 12                  |

- Simple algorithm
- A fictitious current signal is not required
- Frequency-independent operation
- Accurate reference-current extraction
- Fast transient response

Table II summarizes the transient time and THD of the source current with the same resistive plus rectifier load used for Fig. 13 and for different cut-off frequency values of LPF. As expected, the higher the cut-off frequency, the faster is the transient response and THD is higher.

In the last study, shown in Fig. 14, the load on the system is abruptly changed to approximately 200% and then the DC-link voltage recovers in around 400 ms, exhibiting a smooth transient performance and confirming the slow bandwidth with sufficient stability margin of the DC-link voltage loop.

#### VII. CONCLUSIONS

A novel method in the synchronous reference frame for extraction of the reference-compensating current for single-phase APFs is proposed in this study. The major advantages of this technique are summarized as follows:

The effectiveness of the proposed method is investigated by conducting a detailed mathematical analysis and is confirmed by the experiments.

#### **APPENDIX**

A. Derivation of Mathematical Expressions for  $\bar{i}_{dq}$  [Fig. 6]

From Fig. 6, in the Laplace domain, we obtain

$$\overline{i_d}(s) = \frac{\omega_c}{s + \omega_c} \times \ell \left[ i_d(t) + \overline{i_d}(t) \cos(2\omega t) + \overline{i_q}(t) \sin(2\omega t) \right]$$
(26)

$$\overline{i_q}(s) = \frac{\omega_c}{s + \omega_c} \times \ell \left[ i_q(t) + \overline{i_d}(t) \sin(2\omega t) - \overline{i_q}(t) \cos(2\omega t) \right]$$
(27)

where  $\ell$  denotes the Laplace operator.

Multiplying both sides of (26) and of (27) by  $(s + \omega_c)$  and rearranging the results yield

$$\overline{i_d}(s) = \omega_c \times \ell \left[ i_d(t) + (\cos(2\omega t) - 1) \overline{i_d}(t) + \sin(2\omega t) \overline{i_q}(t) \right]$$
(28)

$$\overline{i_q}(s) = \omega_c \times \ell \left[ i_q(t) + \sin(2\omega t) \overline{i_d}(t) - (\cos(2\omega t) + 1) \overline{i_q}(t) \right]$$
(29)

Converting (28) and (29) into the time-domain and rearranging the results into matrix form yield

$$\begin{bmatrix} \dot{\bar{i}}_{d}(t) \\ \dot{\bar{i}}_{q}(t) \end{bmatrix} = \omega_{c} \begin{bmatrix} \cos(2\omega t) - 1 & \sin(2\omega t) \\ \sin(2\omega t) & -\cos(2\omega t) - 1 \end{bmatrix} \begin{bmatrix} \dot{\bar{i}}_{d}(t) \\ \dot{\bar{i}}_{q}(t) \end{bmatrix} + \begin{bmatrix} \omega_{c} & 0 \\ 0 & \omega_{c} \end{bmatrix} \begin{bmatrix} i_{d}(t) \\ i_{q}(t) \end{bmatrix}$$
(30)

Substituting (12) and (13) into (30) and rearranging the results yield

$$\begin{bmatrix} \dot{i}_{d}(t) \\ \dot{i}_{q}(t) \end{bmatrix} = \omega_{c} \begin{bmatrix} \cos(2\omega t) - 1 & \sin(2\omega t) \\ \sin(2\omega t) & -\cos(2\omega t) - 1 \end{bmatrix} \begin{cases} \begin{bmatrix} \dot{i}_{d}(t) \\ \dot{i}_{q}(t) \end{bmatrix} \\ - \begin{bmatrix} I_{m}\cos(\phi) \\ I_{m}\sin(\phi) \end{bmatrix} \end{cases}$$
(31)

From (31), the state-space description of the DFOC method can be derived as follows:

$$\begin{cases} \dot{x}(t) = A(t)x(t) + B(t)u(t) \\ y(t) = Cx(t) \end{cases}$$
 (32)

Where

$$x(t) = \begin{bmatrix} \overline{i_d}(t) \\ \overline{i_q}(t) \end{bmatrix}; \quad u(t) = \begin{bmatrix} I_m \cos(\phi) \\ I_m \sin(\phi) \end{bmatrix}$$

$$A(t) = -B(t) = \omega_c \begin{bmatrix} \cos(2\omega t) - 1 & \sin(2\omega t) \\ \sin(2\omega t) & -\cos(2\omega t) - 1 \end{bmatrix}$$

$$C(t) = I$$

The state-space equation (32) describes a two-input, two-output, and linear time-variant system. The analytical solution of (32)

is highly complicated and is beyond the scope of this study. Thus, the final solution of the state-space equation (32), which is expressed in (17) and (18), is presented. Detailed information on solving the linear time-variant systems can be found in [20] and [21].

B. Determination of the Transfer Function between  $i_{\alpha f}$  and  $i_L$  [Fig. 8]

Substituting (6) and (7) into (30) yields

$$\begin{bmatrix} \dot{i}_{d}(t) \\ \dot{i}_{q}(t) \end{bmatrix} = A(t) \cdot \begin{bmatrix} \dot{i}_{d}(t) \\ \dot{i}_{q}(t) \end{bmatrix} + 2\omega_{c} \begin{bmatrix} \sin(\omega t) \\ -\cos(\omega t) \end{bmatrix}$$
(33)

From Fig. 8, we obtain

$$\left[\frac{\vec{i}_{d}(t)}{\vec{i}_{q}(t)}\right] = T(t) \begin{bmatrix} i_{\alpha f}(t) \\ i_{\beta f}(t) \end{bmatrix}$$
(34)

Substituting (33) into (32) gives

$$\frac{d}{dt} \left\{ T(t) \cdot \begin{bmatrix} i_{\alpha f}(t) \\ i_{\beta f}(t) \end{bmatrix} \right\} = A(t)T(t) \cdot \begin{bmatrix} i_{\alpha f}(t) \\ i_{\beta f}(t) \end{bmatrix} + 2\omega_c \begin{bmatrix} \sin(\omega t) \\ -\cos(\omega t) \end{bmatrix} i_L$$
(35)

Expanding the left-hand side term of (35), rearranging the results, and then multiplying both sides by  $T(t)^{-1}$  yield

$$\frac{d}{dt} \begin{bmatrix} i_{\alpha f}(t) \\ i_{\beta f}(t) \end{bmatrix} = T(t)^{-1} \cdot \left( A(t)T(t) - \frac{dT(t)}{dt} \right) \cdot \begin{bmatrix} i_{\alpha f}(t) \\ i_{\beta f}(t) \end{bmatrix} + 2\omega_{c}T(t)^{-1} \begin{bmatrix} \sin(\omega t) \\ -\cos(\omega t) \end{bmatrix} i_{L}$$
(36)

where  $T(t)^{-1}$  denotes the inverse of matrix T(t).

After a number of simple mathematical manipulations, we derive

$$\frac{d}{dt} \begin{bmatrix} i_{\alpha f}(t) \\ i_{\beta f}(t) \end{bmatrix} = \begin{bmatrix} -2\omega_c & -\omega \\ \omega & 0 \end{bmatrix} \begin{bmatrix} i_{\alpha f}(t) \\ i_{\beta f}(t) \end{bmatrix} + \begin{bmatrix} 2\omega_c \\ 0 \end{bmatrix} i_L \quad (37)$$

With  $i_{\alpha f}$  as the output signal, the state-space representation of the proposed method can be expressed as

$$\begin{cases} \dot{x}(t) = A'(t)x(t) + B'(t)u(t) \\ v(t) = C'x(t) \end{cases}$$
 (38)

where

$$x(t) = \begin{bmatrix} i_{\alpha f}(t) \\ i_{\beta f}(t) \end{bmatrix}; \quad u(t) = i_{L}$$

$$A'(t) = \begin{bmatrix} -2\omega_{c} & -\omega \\ \omega & 0 \end{bmatrix}; \quad B'(t) = \begin{bmatrix} 2\omega_{c} \\ 0 \end{bmatrix}$$

$$C'(t) = \begin{bmatrix} 1 & 0 \end{bmatrix}$$

The state-space equation (38) describes a single-input, single-output, and linear time-invariant system. Thus, the system transfer function can be obtained as follows:

$$G_{DFOC} = \frac{i_{\alpha f}(s)}{i_{I}(s)} = C' [sI - A']^{-1} B' = \frac{2\omega_{c}s}{s^{2} + 2\omega_{c}s + \omega^{2}}$$
(39)

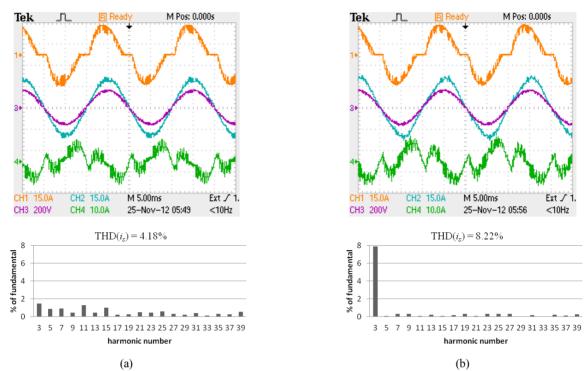


Fig. 12. Experimental waveforms and harmonic spectrum of the source current under a highly non-linear load (a) with and (b) without the use of the DFOC block: (CH1) load current, (CH2) source current, (CH3) grid voltage, and (CH4) APF output current.

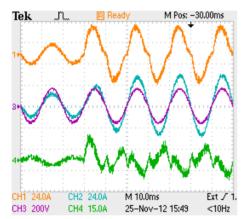


Fig. 13. Experimental waveforms when a rectifier load is added to the resistive load: (CH1) to (CH4) as described in Fig. 12.

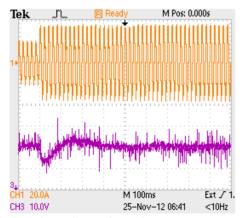


Fig. 14. Experimental waveforms in response to a step load change: (CH1) load current and (CH3) DC-link voltage.

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