

Design of a High-Voltage Piezoelectric Converter for Airbag Ignition Modules

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Abstract

Due to the requirements for high reliability and accuracy, safety issues for airbag ignition systems need to be studied. In this paper, a high-voltage piezoelectric converter is designed to improve these requirements in airbag ignition systems. The proposed converter includes an inverter drive circuit, a Rosen piezoelectric transformer (PZT), an output circuit and a feedback control circuit. The key components of the high-voltage piezoelectric transformer are analyzed in detail. In addition, the proposed converter system is simulated and implemented for testing. The experimental results show that when the power supply is turned on, the charging time is less than 800ms. Furthermore, the output voltage of this converter can be kept between 2.9kV and 3.1kV, under high-efficiency constant current charging mode and zero-voltage switching conditions.

Key words: Constant current charging, Electronic safety, High-voltage, Piezoelectric converter, Zero-voltage switching

I. INTRODUCTION

One of the most important parts of a vehicle to protect drivers and passengers is the airbag ignition system, which is shown in Fig.1. When an accident happens, the crash sensors send collision signals to a SRS-ECU (Supplemental Inflatable Restraint System, Electronic Controller Unit). Then the SRS-ECU enables the ignition module in the airbag system through an ignition voltage. Finally, the ignition module allows inflatable agents in the gas generator to release large amounts of nitrogen into the airbag, which instantly becomes larger to protect people in the vehicle. The major problem in existing airbag ignition modules is weak reliability caused by a low value of the ignition voltage, which is usually from 5 to 30V. Due to this problem, airbags cannot be reliably triggered. To make matters worse, airbags are sometimes

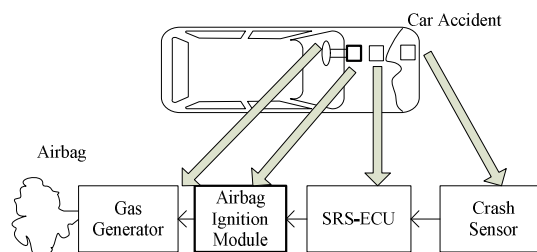


Fig.1. A schematic diagram of an airbag system.

triggered without an accident. This can occur due to electromagnetic interference in the car, because the electrostatic voltage sometimes reaches up to 1kV which can easily trigger the airbag ignition system. Thus, developing a high voltage converter is both necessary and essential. Furthermore, due to the particularity of automotive applications, the charging time for the high voltage converter should be very fast, and the high voltage converter should immediately work when the car is started. The proposed high-voltage converter system solves both problems with a high ignition voltage output from 2.9kV to 3.1kV, and a fast charging time, which is less than 800ms.

Compared with traditional electromagnetic induction transformers, the piezoelectric transformer has the advantages

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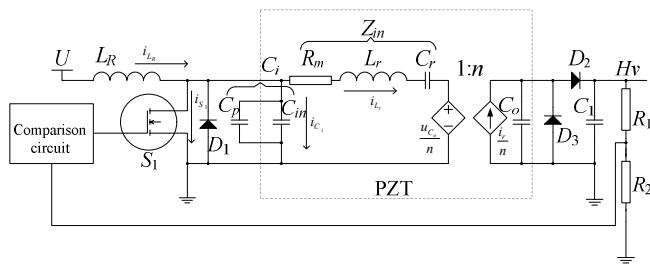


Fig. 2. The piezoelectric high-voltage converter.

of a high power density, low electromagnetic noise, good electrical isolation of the input and output ports, a low cost, mass production capability, etc. However, PZTs have not been adopted in the design of automobile airbag ignition systems [1]. The current research on PZTs is focused on driving cold cathode fluorescent lamps, and PZTs have not been utilized in charging capacitors [2], [3]. The purpose of this study is to apply a PZT to the proposed high-voltage converter system in an airbag ignition system. The high-voltage output of the PZT at a proper operation frequency is used to improve the system stability and reliability under some constraints such as a limited supply voltage, a quick charge time (less than 1s) and a small size.

The switching drive frequency is a key parameter of the proposed high-voltage PZT converter system. The selection of this frequency directly affects the efficiency and the voltage gain of the PZT. In addition, this frequency should be able to drive a switch to achieve soft switching conditions for the high-voltage PZT converter to accomplish constant current charging (CCC). Currently, there are few studies on the charging power of the PZT [4], and a frequency analysis of the quantum control is not directly available in such fields. Although the quantum control mode [5] can facilitate the improvement of the system reliability, some constraints cannot be eliminated because of the driving frequency determination. In this paper, it is realized by both zero-voltage switching (ZVS) and CCC to optimize the choice of the drive frequency.

II. PROPOSED HIGH-VOLTAGE PIEZOELECTRIC CONVERTER

The proposed high-voltage piezoelectric converter system for electronic safety contains a main circuit and a quantum control circuit, as shown in Fig. 2. The main circuit includes an inverter drive circuit, a PZT and an output circuit. The inverter drive circuit has a switch circuit and an input matching circuit, while the output circuit consists of a rectifier circuit and a load circuit. In this figure, Z_{in} represents the impedance of the PZT including R_m , L_r and C_r ; while C_{in} is the input capacitance of the PZT and $C_i = C_p || C_{in}$. The feedback control circuit operates in the quantum control mode.

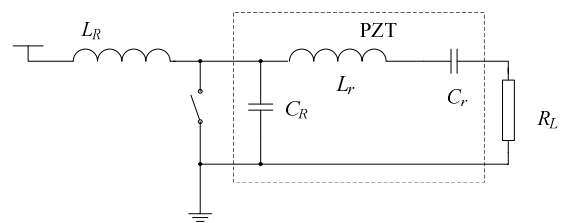


Fig. 3. A simple switching converter circuit.

Through inverter switch S_1 , a whole operation cycle includes one turning on and one turning off. When S_1 is turned on, the DC power charges the inductor L_R . When S_1 is off, the energy stored in the inductor discharges and begins to charge the PZT input capacitor. Then a quasi-sinusoidal voltage waveform is formed at the input of the PZT. The output voltage of the PZT is then raised up in turn. Through the doubler-voltage rectifier circuit, a pulse of DC current is generated to charge the high voltage capacitor C_1 . A resistor divider sends the output voltage to the feedback control circuit. Afterwards a control action on switch S_1 is conducted through the comparison circuit module. If the control circuit detects the output level reaching a preset maximum, the converter drive circuit is turned off. If the opposite situation occurs i.e. the output level is lower than the preset value, the inverter drive circuit will work again. Through this process, the output voltage is controlled to within a preset range.

To drive switch S_1 , an oscillator to generate a signal with a constant frequency and duty cycle is set for the switch. Since the switch input is typically a square wave signal, the output voltage is usually a sine wave or a quasi-sine wave signal through the inverter drive circuit. In the high-voltage converter circuit of an electronic safety system, the working voltage on the switch is several times that of the maximum DC power supply. The self-resonant frequency of the PZT is relatively high; and the switch in the inverter drive circuit must work in the soft-switching state in order to effectively reduce the switch power consumption. In addition, the inverter drive circuit should be flexible to adjust the output amplitude of the quasi-sinusoidal voltage, so as to adjust the output of the PZT to meet the design requirements. Because of the space constraint in this design, the converter adopts a single switch, which is shown in Fig. 3 together with the PZT.

The above circuit drives the PZT transformer with an inductor L_R to achieve high efficiency. This inductor is connected in series to form a low-pass filter, which makes the input voltage waveform nearly sinusoidal. This enables the PZT transformer to operate in low-voltage stress, and removes the impact from high harmonics. It also reduces the loop current and the energy loss of the converter, and lets the power MOSFET inverter work in ZVS. In addition, the ZVS condition, which does not depend on the parameters of the PZT itself, can reduce the EMI noise produced by the converter on the system power source.

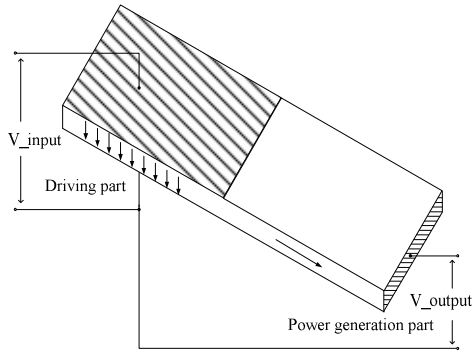


Fig.4. The Rosen-type PZT transformer.

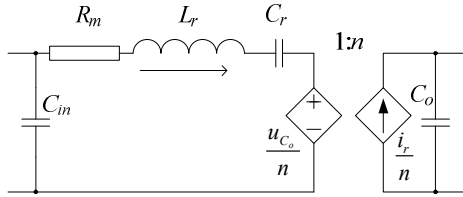


Fig.5. The improved equivalent circuit model of PZT.

Because of its high step-up ratio and larger output power, a Rosen-type PZT transformer is selected for the proposed design, as shown in Fig.4. The PZT consists of two parts: the input and the output, which are also known as the driving part and the power generation part, respectively. The driving part has a fired silver electrode along the thickness of the PZT, while the fired silver electrode of the power generation part is along the length of the PZT. The arrows in Fig.4 represent the direction of polarization in each part. For any PZT, the longer the length, the higher the step-up ratio. As a result, the length of the PZT should be made much longer than its thickness, and the output impedance and voltage should have larger values when compared with their input. Under normal circumstances, when the input voltage is in the range of several volts to tens of volts, the output voltage of the PZT is able to reach several thousand volts or more. In this design, up to 3.1kV of output voltage has been obtained.

In addition, the traditional ideal transformer model has been replaced by two independent controlled sources, as shown in Fig. 5. They are a controlled voltage source at the primary side and a controlled current source at the secondary side. Their parameters are either measured or calculated in Table I. Here, C_{in} stands for the input capacitor, R_m denotes the leakage resistor, C_r and L_r are the resonant capacitor and inductor respectively, C_o is the output capacitor, and n signifies the transfer ratio.

In the DC high-voltage output of the PZT converter, the doubler-voltage rectifier circuit is the first choice for the PZT output. Doubler-voltage rectifier circuits are usually classified as an asymmetric topology with one or two capacitors or as a symmetrical half-bridge topology. Fig. 6 is a type of structures among them, which contains one

 TABLE I
 THE TRANSFORMER PARAMETERS

C_{in}	10.1nF
R_m	10.71 Ω
C_r	301pF
L_r	15.12mH
C_o	18.37pF
n	10.25

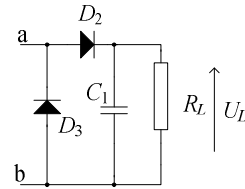


Fig.6. Topology of the doubler-voltage rectifier circuit.

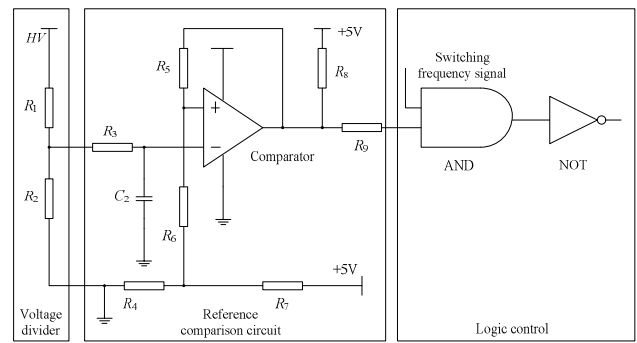


Fig.7. Comparison and logic control circuit.

capacitor C_1 and two diodes. It meets the system requirements simply in terms of the converter design.

The quantum control mode applies the converter output voltage as a feedback to control the circuit. This mode employs an oscillator to generate a drive signal with a constant frequency and duty cycle. Under the effect of this drive signal, the main circuit output level starts to rise. The control signal of the switching input is a high-frequency signal modulated by a low-frequency signal. The high-frequency signal is the selected switch drive signal, and the low-frequency signal is determined by both the upper and lower output voltage, and the discharge time constant. The PZT is able to filter out the low frequency signal which does not affect the performance of the converter. Under the loading short-circuit and no loading conditions, the converter can provide good self-protection. Therefore, no extra over-voltage or over-current protection circuits are required.

The comparison and logic control circuit is shown in Fig. 7. The sampled input signal from the output voltage enters into the comparator through the filter, and resistors R_4 - R_7 are used to setup the upper and lower limits of the reference values. The logic control circuit is implemented by a NAND gate combined with a drive frequency signal.

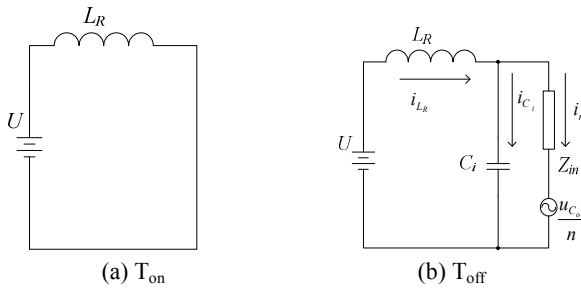


Fig.8. The equivalent circuit of an inverter driver.

III. ANALYSIS AND DESIGN OF A HIGH-VOLTAGE PIEZOELECTRIC CONVERTER

A. Analysis and Design of an Inverter Drive Circuit in the Zvs Condition

As for the traditional switch, during the periods of on or off, the voltage and current waveforms across the switch overlap (neither U nor I is equal to zero), resulting in a switching loss ($P=U * I$). The way to reduce this switching loss is to achieve soft switching. Through the resonance of the inductor and capacitor, the switch voltage (or current) will be a sinusoidal or quasi-sine wave. When the current crosses zero naturally, the switch is turned off, which is so-called ZCS (zero current switching). When the voltage falls to zero, the device is turned on, which is ZVS. Since $U=0$ or $I=0$, then $P=0$. Through the adjustments of C_i and L_R in Fig. 2, the voltage across switch S_1 can reach zero so as to operate in ZVS.

In accordance with the states of an inverter switch, an inverter drive circuit has two modes, mode 1 (switch on) and mode 2 (switch off). The equivalent circuit of the PZT primary part is shown in Fig. 8. The secondary part and the output circuit of the PZT will be discussed in B. *Analysis and design of the charging circuit.*

In mode 1, when $t = t_0$, switch S_1 is on. Since the resistance of S_1 is small, the PZT is shorted and the DC power charges the inductor L_R linearly. Assume that the resistances of the inductor and the switch-on can be ignored. Then the circuit in Mode 1 is described by:

$$L_R \frac{di_{L_R}}{dt} = U \quad (1)$$

Therefore:

$$i_{L_R}(t) = \frac{U}{L_R}(t - t_0) + i_{L_R}(t_0) \quad (2)$$

In mode 2, when $t = t_1$, switch S_1 is off. As a result, the inductor discharges and begins to charge the input capacitor of the PZT to form a resonance voltage. In order to obtain the ideal condition of ZVS, the selected switch drive frequency should be slightly larger than the operating frequency of the PZT. As a result, the resonant branch impedance of the PZT shows inductance characteristics. Meanwhile, the capacitance C_i of the PZT provides a lower impedance path, so that the

inductor L_R and the capacitor C_i are the major resonant components for the inverter. There are the following relationships in Mode 2:

$$L_R \frac{di_{L_R}}{dt} + U_{C_i} = U \quad (3)$$

$$C_i \frac{du_{C_i}}{dt} = i_{C_i} \quad (4)$$

$$i_{L_R} = i_{C_i} \quad (5)$$

Combining (3)-(5), results in:

$$U_{S_1}(t) = Z \bullet i_{L_R}(t_1) \sin \omega_0(t - t_1) + [1 - \cos \omega_0(t - t_1)]U \quad (6)$$

$$i_{L_R}(t) = \frac{U}{Z} \sin \omega_0(t - t_1) + i_{L_R}(t_1) \cos \omega_0(t - t_1) \quad (7)$$

in which:

$$\omega_0 = \frac{1}{\sqrt{L_R C_i}}, Z = \sqrt{\frac{L_R}{C_i}}, u_{S_1} = u_{C_i}$$

Equation (6) shows that the peak voltage of switch S_1 changes with the size of the duty cycle D in the case of the given PZT and the resonant inductor. To reduce the voltage stress on the switch, the value of D should not be too large. Actually, D should be determined by a compromise between the transformer step-up ratio, the output voltage requirement and the withstanding voltage of the switch.

The inductor L_R and the capacitor C_i of the transformer play the role of a low-pass filter and provide an effective way for the soft-switching mode. By adjusting the value of the inductor L_R , the capacitor C_i , and the drive signal duty cycle, the flexibility to adjust the input voltage of the transformer is achieved.

The authors of [6] proposed that the resonant circuit frequency, which is formed by the inductor L_R and the input capacitor C_{in} of the PZT, should be as close as possible to the work frequency:

$$f_w = \frac{1}{2\pi \sqrt{L_R C_{in}}} \quad (8)$$

However, for the drive signals of any duty cycle, the values of the inductor L_R by this formula are not well suited to accomplish the ZVS condition. In addition, the input voltage of the transformer cannot provide flexible regulation. Combining Equation (6) with the requirement of the ZVS condition, the inverter driver parameters should be selected so that the voltage u_{S_1} is zero in the whole low level of the drive signal u_{GS} . Then the following relationship is available:

$$[\pi + 2 \arctan \frac{U}{Z \bullet i(t_1)}] \sqrt{L_R C_i} \leq \frac{1}{f_d} (1 - D) \quad (9)$$

where f_d is the drive frequency.

Here, C_{in} is substituted by C_i (C_{in} , C_p). The parameters L_R , C_i , and the duty cycle D are determined after obtaining the maximum voltage of the transformer input and the drive signal frequency by the combination of Equations (6) and (9). As a result, the proposed scheme is able to achieve the

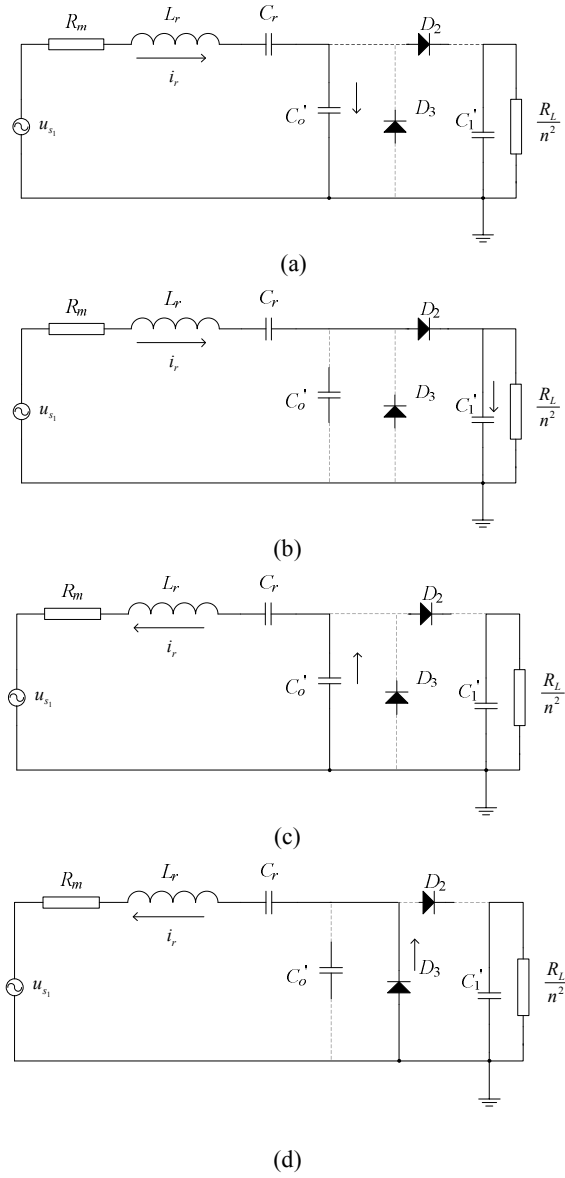


Fig.9. The equivalent circuit of the converter working states.

flexible adjustment of the input voltage of the transformer, and realize the ZVS condition.

B. Analysis and Design of the Charging Circuit

Unlike the analysis of general series and parallel resonant converters, the voltage of a charged capacitor is a variable. During the charging time, the output voltage gradually increases. Suppose that the circuit components work in the ideal state, and the full charging cycle of the converter is divided into four states. The output capacitance of the secondary of the PZT, C_o , is mapped to the primary and it is denoted as C'_o , and the loaded high voltage capacitance C_1 is mapped as C'_1 . The state analysis for the equivalent circuit is as follows, where $C'_o = n^2 C_o$, and $C'_1 = n^2 C_1$.

State 1 [t_0-t_1]: In order to facilitate the analysis, the zero

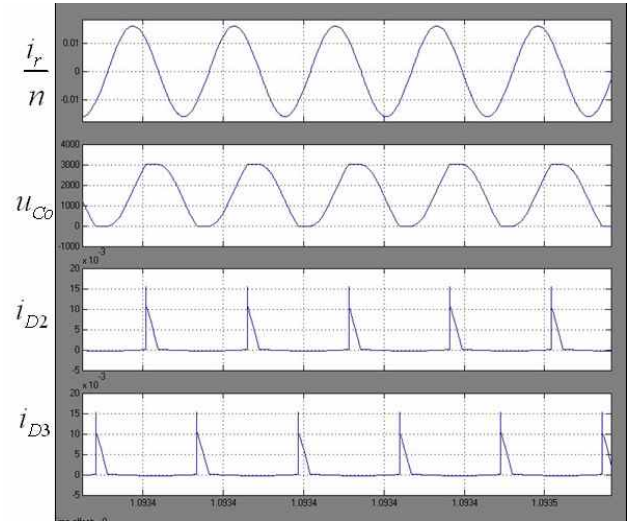


Fig.10. Current and voltage waveforms of doubler-voltage rectifier circuit.

point of the positive current is regarded as the time zero point t_0 , and the equivalent circuit is illustrated in Fig. 9 (a). At t_0 , the resonant current i_r starts to charge the capacitor C'_o from zero. Then it generates a sine wave in resonance, which means the circuit is in the internal resonant state and does not provide energy to the load. When the voltage of C'_o rises to $U_{c_p} = U_L / n$, this state ends.

State 2 [t_1-t_2]: At t_1 , diode D_2 is turned on, and the circuit begins to generate energy to the load. At this time, C'_o quits the resonance and C'_1 takes part in the resonant circuit. Usually $C'_1 \ll C_r$, and the resonant characteristic of the circuit is almost free from the influence of C'_1 . When the positive resonant current is reduced to zero, this state ends. Fig. 9 (b) illustrates the equivalent circuit of this state.

State 3 [t_2-t_3]: At t_2 , the positive resonant current reduces to zero and D_2 is turned off. Then C'_o joins the resonant circuit. The circuit stops sending energy to the load and works in the internal resonance. The voltage of C'_o begins to decline. When it reaches zero, this state ends. Fig. 9 (c) shows the equivalent circuit of this state.

State 4 [t_3-t_4]: At t_3 , diode D_3 is switched on, and C'_o quits the resonance. This state is completed and begins to return to state 1. This means that an entire cycle has finished. Fig. 9 (d) illustrates the equivalent circuit for this state.

The current i_r/n flows into the transformer output capacitor C_o when the two rectifier diodes are in their periods of non-conducting. Mainly, there are two rectifier diode conduction periods, and the voltage of the output capacitor C_o is equal to the high-voltage of the load in the D_2 period and is zero in the D_3 period. The related current and voltage waveforms are shown in Fig. 10.

When the voltage of the output capacitor C_o for the PZT is in the diode non-conduction period, it is divided into interval functions of Equation (10) according to the phase intervals and initial values [7].

$$u_{c_o} = \begin{cases} \frac{U_L}{1 + \cos \theta} (1 - \cos \vartheta) & 0 < \vartheta < \pi - \theta \\ U_L & \pi - \theta < \vartheta < \pi \\ \frac{U_L}{1 + \cos \theta} (\cos \theta - \cos \vartheta) & \pi < \vartheta < 2\pi - \theta \\ 0 & 2\pi - \theta < \vartheta < 2\pi \end{cases} \quad (10)$$

where U_L is the voltage of the load, θ is the conduction angle, and ϑ is the phase.

It is shown that the CCC mode is able to maximize the capacitor charge efficiency, which is an important quality standard for evaluating capacitor charging performance. The next process is a detailed analysis for the converter to achieve the CCC condition.

The charging power supply of the capacitor is different from the normal charging process of a DC power supply. The former output voltage steps up with increments of the duty cycles. Here the corresponding diode conduction angle, the maximum resonant current and the initial output voltage of the m^{th} working cycle are expressed as θ_m , $I_{rm(m)}$ and $U_{L(m-1)}$, respectively. Equation (10) gives:

$$\frac{I_{rm(m)}}{n} = \frac{U_{L(m-1)} \omega C_o}{1 + \cos \theta_m} \quad (11)$$

where $\omega = 2\pi f$. Thus, the average current through diode D_2 at any m^{th} working cycle is:

$$\begin{aligned} I_{D2av} &= \frac{1}{2\pi} \int_{\pi - \theta_m}^{\pi} \frac{I_{rm(m)}}{n} \sin \vartheta d\vartheta \\ &= \frac{1}{2\pi} \frac{I_{rm(m)}}{n} (1 - \cos \theta_m) \\ &= \frac{1}{2\pi} U_{L(m-1)} \omega C_o \tan^2 \left(\frac{\theta_m}{2} \right) \end{aligned} \quad (12)$$

In this cycle, the quantity of charging is:

$$Q_m = I_{D2av} \cdot \frac{2\pi}{\omega} \quad (13)$$

Assuming the linear CCC condition of the capacitor and ignoring the energy consumption by the load resistor divider, the quantity of the charging in the m^{th} cycle is equal to the $(m-1)^{\text{th}}$ cycle:

$$Q_m = Q_{m+1} \quad (14)$$

Substituting Equations (12) and (13) into (14), yields:

$$U_{L(m-1)} C_o \tan^2 \left(\frac{\theta_m}{2} \right) = U_{L(m)} C_o \tan^2 \left(\frac{\theta_{m+1}}{2} \right) \quad (15)$$

And:

$$U_{L(m)} = U_{L(m-1)} + Q_m / C_1 \quad (16)$$

From equations (15) and (16), the recurrence formula of

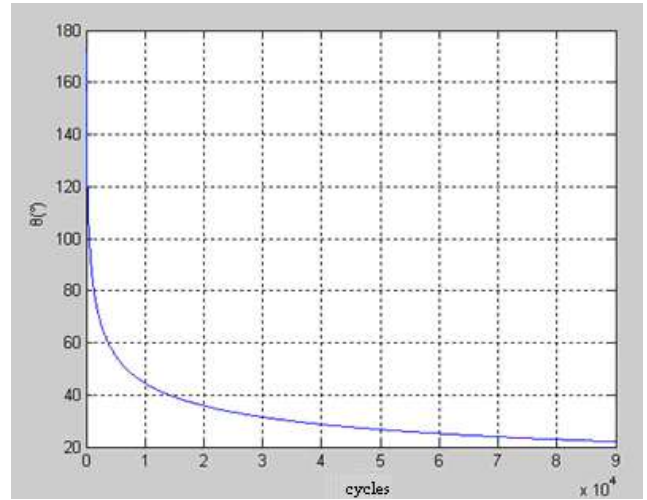


Fig.11. Relationship between conduction angle and cycles.

the conduction angle change in the CCC condition is [8]:

$$\tan^2 \left(\frac{\theta_{m+1}}{2} \right) = \frac{\tan^2 \left(\frac{\theta_m}{2} \right)}{1 + \frac{C_o}{C_1} \cdot \tan^2 \left(\frac{\theta_m}{2} \right)} \quad (17)$$

The relationship between the calculated conduction angle (using the numerical analysis by equation (17)) and the number of the operation cycles is shown in Fig. 11. Here, the initial value of θ is π . It can be seen from the graph that with an increase of the charge cycles, the conduction angle decreases. At around 5,000 charge cycles, the conduction angle drops rapidly and after that it gradually slows down. Finally the conduction angle reaches a stable value.

Equation (11) shows that the resonant currents of the $(m+1)^{\text{th}}$ and m^{th} working cycles meet the relationship:

$$\frac{I_{rm(m+1)}}{I_{rm(m)}} = \frac{(1 + \cos \theta_{m+1}) U_{L(m)}}{(1 + \cos \theta_m) U_{L(m-1)}} \quad (18)$$

When the constant-current charging in equal-step is achieved by the high-voltage capacitor of the converter, it is clear that:

$$\begin{aligned} U_{L(m)} &= m \cdot \Delta U \\ U_{L(m+1)} &= (m+1) \Delta U \end{aligned} \quad (19)$$

Therefore, from equation (18), the maximum resonant current should satisfy the following equation to achieve the CCC condition:

$$\frac{I_{rm(m+1)}}{I_{rm(m)}} = \frac{m}{m-1} \quad (20)$$

This denotes $\Delta I_{rm(m)} = I_{rm(m+1)} - I_{rm(m)}$, then:

$$\Delta I_{rm(m)} = I_{rm(m+1)} / m \quad (21)$$

and:

$$I_{rm(m+1)} = m \times \Delta I_{rm(m)} \quad (22)$$

Equation (22) shows that when the maximum of resonant

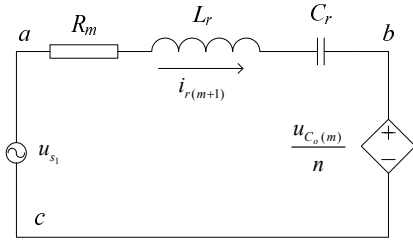


Fig. 12. The primary side equivalent circuit of the PZT.

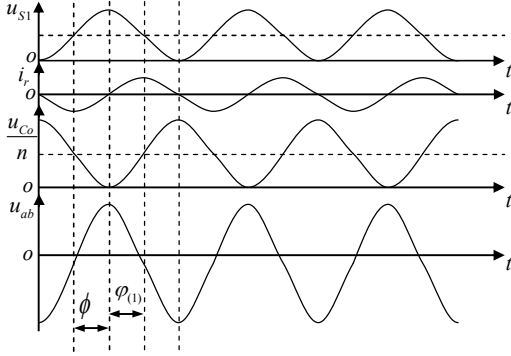
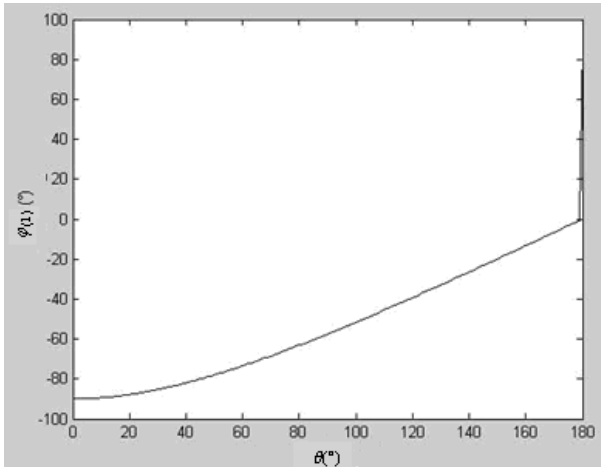


Fig. 13. The main waves of the resonant branch.


 Fig. 14. Relationship between ϕ_1 and θ .

current is small at the beginning and increases in equal-step, the capacitor of the converter is approximately charged in equal-step.

C. Analysis and Design of the Drive Frequency

Considering the primary side of the PZT in the converter circuit, the equivalent circuit is shown in Fig. 12. When the load capacitor is charged by the converter, the main curves are presented in Fig. 13.

In the fixed-frequency control mode, the key consideration in the charging circuit is the late stage of charging. In the latter part of the output capacitor charging procedure, the phase of the voltage fundamental wave is a quarter cycle lag when compared with the phase of the resonant current, which is shown as $\phi_{(1)}$ in Fig. 14.

At the selected frequency, the impedance between nodes a and b in Fig. 12 is definite. Combining this property with the equal-step charging condition given by Equation (20), the proposed system is able to approximately meet the CCC condition, as long as the maximum value of V_{ab} is increased in equal-step. Fig. 13 shows the phase differences among u_{s1} , i_r and u_{Co}/n at the same time. If the phase difference between the resonant current and the input voltage is about 90° , as shown in Equation (23), it is capable of meeting the requirements of the above analysis.

$$\arctan\left(\frac{R_m}{\sqrt{\left(L_r\omega_s - \frac{1}{C_r\omega_s}\right)^2}}\right) = \phi \rightarrow 90^\circ \quad (23)$$

where ω_s is the serial resonant frequency.

As a result, the corresponding drive frequency is:

$$f_d = \frac{\omega_s}{2\pi} \quad (24)$$

At this time, u_{s1} and $u_{Co(1)}/n$ have the reverse phase. Therefore, their peak-to-peak voltage can be a maximum value. Because of the increasing trend of $u_{Co(1)}/n$, the peak-to-peak value of u_{ab} rises in equal-step as follows:

$$u_{ab(p-p)} = u_{s1(p-p)} + \frac{U_{Co(1)(p-p)}}{n} \quad (25)$$

The corresponding peak-to-peak value of the resonant current is:

$$i_{r(p-p)} = \frac{u_{ab(p-p)}}{\sqrt{R_m^2 + \left(L_r\omega_s - \frac{1}{C_r\omega_s}\right)^2}} \quad (26)$$

It shows that when the charge cycles increase, the conduction angle decreases in the equal-step charging condition. The durations for state 1 and state 3 enlarge progressively while the durations for state 2 and state 4 decline step by step. When the equivalent circuit works in the resonant state, the resonant circuit gets the maximum voltage gain and current values.

The operating frequency for states 2 and 4 are:

$$f'_{w2,4} = \frac{1}{2\pi\sqrt{L_r C_r}} \quad (27)$$

The operating frequency for states 1 and 3 are:

$$f'_{w1,3} = \frac{1}{2\pi\sqrt{L_r \cdot \frac{C_r \cdot n^2 C_o}{C_r + n^2 C_o}}} \quad (28)$$

Equation (28) gives the upper limit of the drive frequency, and this value meets a large step-up ratio in the late state of the charging. Meanwhile, $f'_{w1,3}$ is larger than the actual operating value. Thus, this frequency is able to realize soft-switching to create a good work condition.

Substituting the parameter values of the selected transformer into Equations (24) and (27-28) with a selected $100\text{M}\Omega$ sampling divider resistance, the frequency values for

f_d , $f_{w2,4s}$ and $f_{w1,3}$ are 79.5kHz, 74.6kHz, and 80.2kHz, respectively. In all, the switch drive frequencies are analyzed and calculated from different perspectives, and the results indicate consistency.

D. Design of the Quantum Control Circuit

The quantum control circuit mainly consists of a voltage sampling divider circuit, a reference comparison circuit and a logic control circuit. The sampling circuit obtains the output voltage from a high-voltage capacitor and sends it to the reference comparison circuit. The comparison circuit compares the output voltage to the upper and lower limits of the preset reference voltage and then exports a corresponding high or low level. Together with the comparison of the circuit output signal, the logic control circuit is responsible for generating a selected switching-frequency with a fixed duty cycle. As a result, the modulated output is applied to drive the switch.

The design principle of the sampling resistor divider is to guarantee the capacitor charging efficiency, without affecting the operation characteristics of the PZT. The output power is:

$$\begin{aligned} P_{Lm} &= \frac{(U_{Co(1)\max})^2}{2R_{Leq}} \\ &= \frac{U_{L(m)}^2}{R_L} + \left(\frac{1}{2T_d} C_1 U_{L(m)}^2 - \frac{1}{2T_d} C_1 \left(U_{L(m)} - \frac{U_{L(m)}}{m} \right)^2 \right) \quad (29) \\ &= \frac{U_{L(m)}^2}{R_L} + \left(\frac{1}{2} f_d C_1 U_{L(m)}^2 - \frac{1}{2} f_d C_1 \left(U_{L(m)} - \frac{U_{L(m)}}{m} \right)^2 \right) \end{aligned}$$

From the above equation, the converter's output impedance is deduced as:

$$R_{Leq} = \frac{k_{u(1)}^2 R_L}{8 + 4R_L C_1 f_d \left(\frac{2m-1}{m^2} \right)} \quad (30)$$

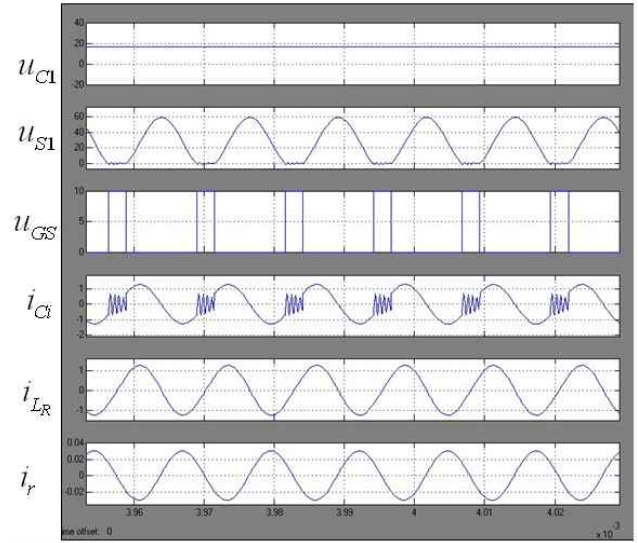
where f_d is the drive frequency, and $k_{u(1)}$ indicates the voltage waveform coefficient [8]. Without a sampling resistor in the converter output, the output power becomes:

$$\begin{aligned} P_{Lm} &= \frac{(u_{Co(1)\max})^2}{2R_{Leq}'} = \frac{1}{2} f_d C_1 U_{L(m)}^2 - \frac{1}{2} f_d C_1 \left(U_{L(m)} - \frac{U_{L(m)}}{m} \right)^2 \\ P_{Lm} &= \frac{(U_{Co(1)\max})^2}{2R_{Leq}'} \\ &= \frac{1}{2} f_d C_1 U_{L(m)}^2 - \frac{1}{2} f_d C_1 \left(U_{L(m)} - \frac{U_{L(m)}}{m} \right)^2 \end{aligned} \quad (31)$$

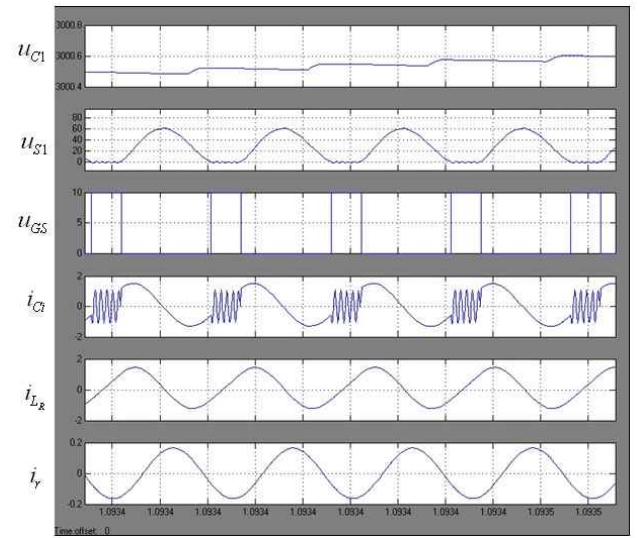
Therefore:

$$\frac{R_{Leq}'}{R_{Leq}} = \frac{2}{R_L f_d C_1 \left(\frac{2m-1}{m^2} \right)} + 1 \quad (32)$$

The power consumption of the voltage sampling resistor should be much less than the converter output power, and the impedance change of the resistor should not affect the transformer's frequency characteristics in the early charging.



(a)



(b)

Fig.15. The main output voltage waveforms.

Moreover, it should ensure that the difference between the equivalent resistances is no more than one order of the magnitude. From Equation (32), $R_L=100M\Omega$ meets this requirement.

IV. SIMULATION

A circuit simulation is carried out by applying MATLAB's SimPowerSystems Tool Box [9]. With the components and related modules provided by the Box, the simulation is performed in strict accordance with the electrical connections of the actual circuit in the modeling. In this way, the simulation model becomes visual and it is relatively simple to adjust the values of the circuit parameters. Fig. 15(a) and (b) present the simulation results of the main associated signal output voltage waveforms. They clearly show that all of the circuit waveforms are consistent with the analysis results. These simulation results verify the correctness of the above

TABLE II
THE EXPERIMENTAL CONFIGURATION AND RESULTS

Components	Parameters/Sizes
S_1	FET IRLR3410
C_1	0.22 μ F/4kV
D_1	GL506
R_1	100K Ω
R_2	100M Ω
D	0.2
L_R	47 μ H
C_P	78nF
Drive signal frequency	79.4kHz
Input DC voltage	28V
Output	Experimental results
u_{S1}	58V
u_{C1}	3.0kV

analysis.

Figures 15 (a) and (b) demonstrate the early charging and late charging wave curves, respectively. They show that the output voltage waveform of the high-voltage capacitor u_{C1} increases in equal step. Here, the voltage waveform u_{S1} is a quasi-sine. It starts from zero when the switch is closed, and the voltage decreases exactly to zero when the switch is opened (u_{GS} is at a high level). The switch operates in the soft-switching mode, which significantly reduces switching losses. Meanwhile, the maximum input voltage u_{S1} is 60V, which is consistent with the measured value of the circuit. It can be seen that the capacitance-voltage curve increases in equal-step in the quantum control mode during the process of output voltage change, which indicates that the high voltage capacitor charging circuit works in a highly efficient CCC mode.

V. EXPERIMENTAL RESULTS

In order to verify the correctness of the proposed converter circuit, a prototype was developed. The parameters of the main circuit components and the experimental results of the prototype converter are shown in TABLE II.

It is mentioned that the calculated value of the drive frequency should be slightly larger than the measurement value. Therefore, the calculated frequency is selected directly, and is able to achieve the ZVS condition.

Experimental waveforms of the drive signal voltage u_{GS} and the transformer input voltage u_{S1} are shown in Fig. 16. Here the input DC voltage, the drive signal frequency and the duty cycle D are set to 28V, 79.4kHz and 0.2, respectively. The inductance of L_R is calculated as 47 μ H. The parallel input capacitance C_P is set to 78nF.

It can be seen that the inverter switch actually works in the ZVS mode and that the maximum of u_{S1} is approximately 58V, which is in accordance with the calculated result from Equation (6).

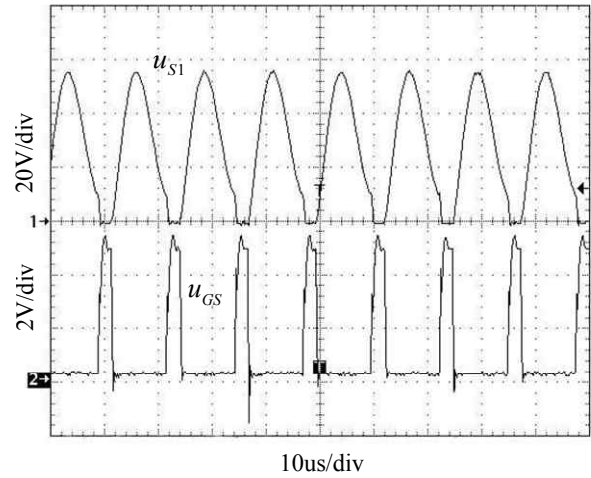


Fig.16. Experiment waveforms of u_{GS} and u_{S1}

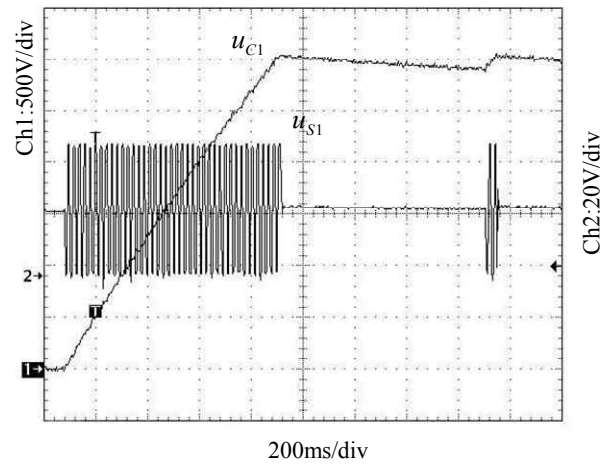


Fig.17. Experiment waveforms of u_{S1} and u_{C1} .

At room temperature, the waveforms of the input voltage and the output voltage of the load capacitance are indicated in Fig. 17. The output voltage is controlled at a range from 2.9kV to 3.1kV in less than 800ms. It is obvious that the charging time from the power-on is quick enough for an airbag to be triggered. This shows that in the quantum control mode, the ascending slope of the capacitance-voltage curve is almost constant in the changing process of the output voltage. This indicates that the high voltage capacitor charges in a highly efficient CCC mode.

Waveforms of the PZT resonant branch current i_r and the output voltage u_{C1} of the load capacitance are shown in Fig. 18. In the late charging process, the envelope curve of the resonant current trend is in accordance with the equal-step output voltage, which verifies the CCC mode. The waveform of the high voltage capacitor in the discharge of a short-circuit is illustrated in Fig. 19. The sharp decline in the curve shows that the circuit is able to continue to work under a load discharge. If there is no-load, the voltage is still controlled at the preset range.

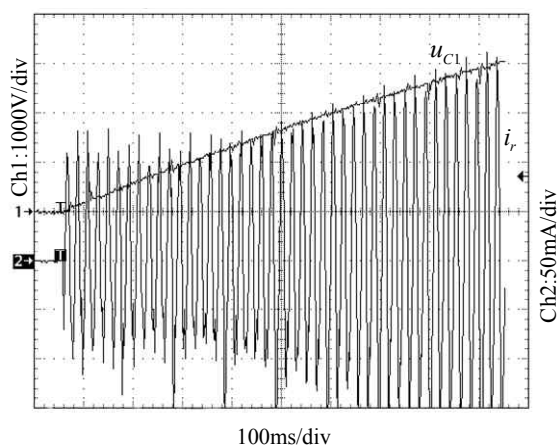


Fig.18. Experiment waveforms of i_r and u_{C1} .

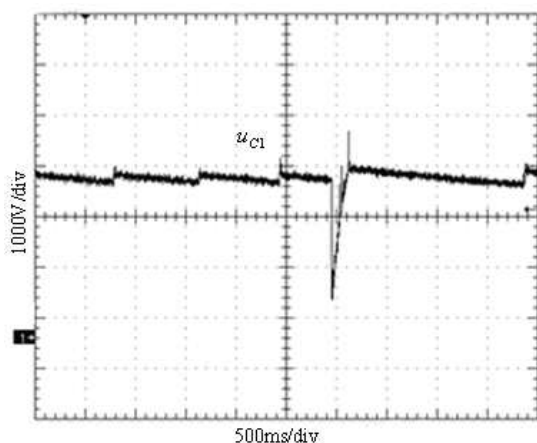


Fig.19. The high voltage capacitor waveform in load short-circuit discharge.

The designed PZT converter has been utilized in an airbag safety ignition system, and the test PCB is shown in Fig. 20. When compared with current airbag ignition systems, the proposed system has a smaller size and less weight because a PZT is utilized. In addition, higher reliability and accuracy are gained through its 2.9kV to 3.1kV ignition energy in a highly efficient mode. When a collision occurs, the high-voltage capacitor triggers the airbag more quickly than the low-voltage ones. Therefore, it is a faster response device than the traditional ones.

VI. CONCLUSIONS

With the advantages of the PZT, a high-voltage piezoelectric converter was designed for applications to improve electronic safety in this paper. The proposed scenario includes the inverter drive circuit, a Rosen PZT, the output circuit, and the feedback control circuit.

The innovation of this design is to combine the PZT with a high-voltage capacitor to charge loads with high-efficiency in safety-related electronic systems. The components of the high-voltage PZT are analyzed and designed in detail. In

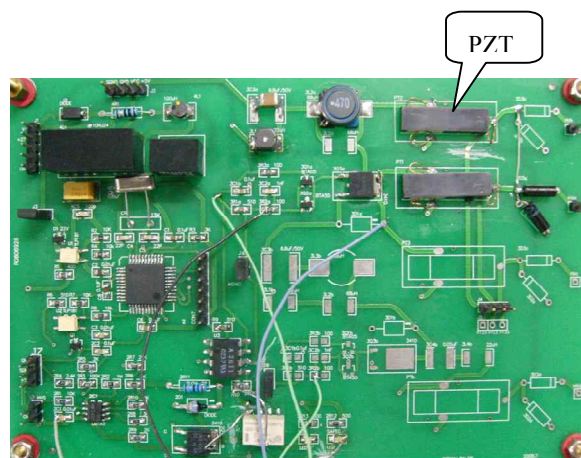


Fig.20. A test PCB of PZT converter.

addition, the implementation of the high-voltage piezoelectric converter system is given. In order to verify the validity of the converter circuits, a prototype was developed. The experimental results are fully in line with the calculation and analysis results. The output voltage of this converter is gained and controlled in a range from 2.9kV to 3.1kV in less than 800ms, which works in the high-efficiency CCC mode with the ZVS condition.

In general, the advantages of the proposed system are its smaller size and lighter weight. Furthermore, through the ignition energy from 2.9kV to 3.1kV in high efficiency mode, higher reliability and accuracy are gained. Finally, the charging speed is controlled in less than 800 ms which means that the proposed system has a faster response time when compared with the traditional ones. With the increased ignition energy produced by the PZT converter, the reliable ignition of airbags is realized in electronic safety systems for intermediate or advanced grade vehicles.

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